
Active AT89C5132 Errata List

- SPI Controller – Bad Transfer in Slave Mode with CPHA = 0
- C51 Core – Bad Exit of Power-down in X2 Mode

AT89C5132 Errata History

Lot Number	Errata List
A00960 and above	1, 2

AT89C5132 Errata Description

1. SPI Controller – Bad Transfer in Slave Mode with CPHA = 0

SPI controller used in slave mode with CPHA = 0 leads to erratic data reception and transmission.

Workaround

Use slave mode only with CPHA = 1.

2. C51 Core – Bad Exit of Power-down in X2 Mode

When exiting Power-down mode by interrupt while CPU is in X2 mode, it leads to bad execution of first instruction executed when CPU restarts.

Workaround

Put the CPU in X1 mode just before entering power-down mode.



**USB
Microcontrollers**

AT89C5132

Errata Sheet

Rev. 4253A–USB–08/03



Active USB Bootloader Errata List

- No Active Errata

USB Bootloader Errata History

Version Number	Errata List
V1.6.0	1
V1.6.2	None

USB Bootloader Errata Description

1. Boot Process - Marginal SBV Value

When Bootloader is called from user's application with SBV = F0h, the bootloader is never executed and execution enters an infinite loop.

Workaround

Always set SBV value to FFh instead of F0h.



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