Polymer Electrolytic (KO-CAP®), 6.3 – 35 VDC



Overview

KOCAP Polymer Capacitors

KEMET's Organic Capacitors (KO-CAP) are preferred solutions for applications requiring power loss protection (hold-up) or maximum power efficiency of a circuit when board space is limited. Desired benefits include high energy density, stable capacitance with applied voltage and temperature, and no aging effects. The conductive polymer cathode of these solid electrolytic capacitors

provide very low ESR and higher capacitance retention at high frequencies. Unlike liquid electrolyte-based capacitors, KEMET's polymer capacitors have a very long operational life and high ripple current capabilities. Capacitors from T520, T521 and T523 series are commonly used in these applications. The T545 and T548 series were introduced to meet specific needs for a subsegment of solid state drives.

Benefits

- · Highest energy per unit volume
- · Stable capacitance across temperature and voltage
- · No aging effects
- · Low ESR values
- · High frequency capacitance retention
- · High ripple handling
- 100% accelerated steady state aging
- 100% surge current tested
- 100% thermal shock tested (T545 series only)
- Halogen-free epoxy/RoHS compliant

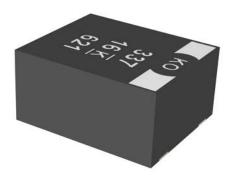
Applications

Typical applications include enterprise storage, networking, server, mobile, client storage, and client computing.

T520/T521/T545



T523/T548





Environmental Compliance

- RoHS compliant (6/6) according to Directive 2002/95/EC when ordered with 100% Sn solder or Ni-Pd-Au
- · Halogen-free
- Epoxy compliant with UL94 V-0

K-SIM

For a detailed analysis of specific part numbers, please visit ksim.kemet.com to access KEMET's K-SIM software. KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels.

Ordering Information

Т	548	V	157	M	016	A	T	E050	
Capacitor Class	Series	Case Size	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage (VDC)	Failure Rate/ Design	Termination Finish	ESR	Packaging (C-Spec)
T = Tantalum	520 = Low Voltage 521 = High Voltage 523 = Facedown Terminal 545 = High Energy 548 = High Energy, Facedown Terminal	B H J M O T V W X	First two digits represent significant figures. Third digit specifies number of zeros.	M = ±20%	006 = 6.3 010 = 10 016 = 16 020 = 20 035 = 35	A = N/A	T = 100% Matte Tin (Sn) plated P* = Ni-Pd-Au plated	ESR in mΩ	Blank = 7" Reel 7280 = 13"Reel

^{*} P termination only available on T523/T548 part numbers

Performance Characteristics

Item	Performance Characteristics
Operating Temperature	-55°C to 85°C/105°C (Refer to part number in Table 1 for maximum temperature rating)
Rated Capacitance Range	22 μF – 1,500 μF at 120 Hz/25°C
Capacitance Tolerance	M Tolerance (20%)
Rated Voltage Range	6.3 – 35 VDC
DF (120 Hz)	Refer to part number in Table 1 for electrical specification
ESR (100 kHz)	Refer to part number in Table 1 for electrical specification
Leakage Current	\leq 0.1 CV (µA) at rated voltage after 5 minutes (Refer to part number in Table 1 for electrical specification)



Qualification

Test	Condition			Characteristics						
			Δ C/C	Within -20/+10% of initial value						
Endurance	OF°C or 10F°C at rated valtage 2 000 hour	·**	DF	Within initial limit						
Endurance	85°C or 105°C at rated voltage, 2,000 hour	S^^	DCL	Within	1.25 x initi	al limit				
			ESR	Within 2	Within 2.0 x initial limit					
			Δ C/C	Within ·	-20%/+10%	% of initial	value			
Storage Life	85°C or 105°C at 0 volts, 2,000 hours**		DF	Within i	nitial limit	S				
Storage Life	83 C OF 103 C at 0 Voits, 2,000 flours		DCL	Within	1.25 x initi	al limit				
			ESR	Within 2.0 x initial limit						
			Δ C/C	Within -5% /+35% of initial value						
Llumiditu	60°C 00% DII No Lood 500 hours		DF	Within i	nitial limit					
Humidity	60°C, 90% RH, No Load, 500 hours		DCL	Within 5.0 x initial limit						
		ESR	Within 2.0 x initial limit							
	Extreme temperature exposure at a		+25°C	-55°C	+25°C	+85°C	+105°C**	+25°C		
Temperature Stability	succession of continuous steps at	Δ C/C	IL*	+/-20%	+/-10%	+/-20%	+/-30%	+/-10%		
remperature Stability	+25°C, -55°C, +25°C, +85°C, +105°C**, +25°C	DF	IL	IL	IL	1.2 x IL	1.5 x IL	IL		
	+23°C	DCL	IL	N/A	IL	10 x IL	10 x IL	IL		
			ΔC/C	Within -	-20/+10%	of initial v	alue			
Surge Voltage	85°C or 105°C, 1.32 x rated voltage, 1,000		DF	Within i	nitial limit	S				
Surge voltage	cycles**		DCL	Within i	nitial limit	S				
			ESR	Within initial limits						
Mechanical Shock/	MIL-STD-202, Method 213, Condition I, 100	G	Δ C/C	Within ±10% of initial value (Within initial limits for T527 Series)						
Vibration	peak		DF	Within i	Within initial limits					
			DCL	Within i	nitial limit	:s				

^{*} IL = initial limit

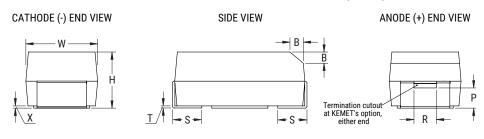
^{**} Refer to Table 1 - Ratings & Part Number Reference for temperature classification. If temperature classification is 85°C, the 105°C step is not performed for the temperature stability test.

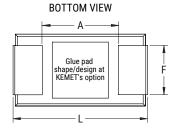


Dimensions - Millimeters (Inches)

Metric will govern

For T520/T521/T545





KEMET	EIA	L	w	Н	F ±0.1 (±0.004)	S ±0.3 (±0.012)	B ±0.15 (Ref) ±0.006	X (Ref)	P (Ref)	T (Ref)	A (Min)	Total Weight (mg)
T	3528-12	3.5±0.2 (0.138±0.008)	2.8±0.2 (0.110±0.008)	1.1±0.1 (0.043±0.004)	2.2 (0.087)	0.80 (0.032)	N/A	0.05 (0.002)	N/A	0.13 (0.005)	1.9 (0.075)	55
М	3528-15	3.5±0.2 (0.138±0.008)	2.8±0.2 (0.110±0.008)	1.4±0.1 (0.055±0.004)	2.2 (0.087)	0.8 (0.031)	N/A	0.05 (0.002)	N/A	0.13 (0.005)	1.1 (0.043)	98
В	3528-21	3.5±0.2 (0.138±0.008)	2.8±0.2 (0.110±0.008)	1.9±0.2 (0.075±0.008)	2.2 (0.087)	0.80 (0.032)	0.4 (0.016)	0.10±0.10 (0.004±0.004)	0.5 (0.020)	0.13 (0.005)	1.9 (0.075)	95
W	7343-15	7.3±0.3 (0.287±0.012)	4.3±0.3 (0.169±0.012)	1.4±0.1 (0.055±0.004)	2.4 (0.094)	1.30 (0.051)	N/A	0.05 (0.002)	N/A	0.13 (0.005)	3.6 (0.142)	223
V	7343-20	7.3±0.3 (0.287±0.012)	4.3±0.3 (0.169±0.012)	1.9±0.1 (0.075±0.004)	2.4 (0.094)	1.30 (0.051)	N/A	0.05 (0.002)	N/A	0.13 (0.005)	3.6 (0.142)	274
Υ	7343-40	7.3 ±0.3 (0.287±0.012)	4.3±0.3 (0.169±0.012)	3.8±0.2 (0.150±0.008)	2.4 (0.094)	1.3 (0.051)	0.5 (0.020)	0.10±0.10 (0.004±0.004)	1.7 (0.067)	0.13 (0.005)	3.8 (0.150)	494
Х	7343-43	7.3±0.3 (0.287±0.012)	4.3±0.3 (0.169±0.012)	4.0±0.3 (0.157±0.012)	2.4 (0.094)	1.30 (0.051)	0.5 (0.020)	0.10±0.10 (0.004±0.004)	1.7 (0.067)	0.13 (0.005)	3.6 0.142)	554
J	7360-15	7.3±0.3 (0.287±0.012)	6.0±0.3 (0.236±0.012)	1.4±0.1 (0.055±0.004)	4.1 (0.161)	1.30 (0.051)	N/A	0.10±0.10 (0.004±0.004)	N/A	0.13 (0.005)	3.3 (0.130)	263
Н	7360-20	7.3±0.3 (0.287±0.012)	6.0±0.3 (0.236±0.012)	1.9±0.1 (0.075±0.004)	4.1 (0.161)	1.3 (0.051)	N/A	0.10±0.10 (0.004±0.004)	N/A	0.13 (0.005)	3.3 (0.130)	385
0	7360-43	7.3±0.3 (0.287±0.012)	6.0±0.3 (0.236±0.012)	4.0±0.3 (0.157±0.012)	4.1 (0.161)	1.3 (0.051)	N/A	0.10±0.10 (0.004±0.004)	N/A	0.13 (0.005)	3.3 (0.130)	696



KEMET	EIA	L	W	н	F ±0.1 (±0.004)	S ±0.3 (±0.012)	Total Weight (mg)
W	7343-15	7.3±0.3 (0.287±0.012)	4.3±0.3 (0.169±0.012)	1.4±0.1 (0.055±0.004)	2.4 (0.094)	1.3 (0.051)	223
J	7360-15	7.3±0.3 (0.287±0.012)	6.0±0.3 (0.236±0.012)	1.4±0.1 (0.055±0.004)	4.45 (0.175)	1.6 (0.063)	263
٧	7343-20	7.3±0.3 (0.287±0.012)	4.3±0.3 (0.169±0.012)	1.9±0.1 (0.075±0.004)	2.4 (0.094)	1.3 (0.051)	274
Н	7360-20	7.3±0.3 (0.287±0.012)	6.0±0.3 (0.236±0.012)	1.9±0.1 (0.075±0.004)	4.45 (0.175)	1.6 (0.063)	385



Table 1 - Ratings & Part Number Reference

Rated Voltage/ Application Voltage	Case Size/ Case Height	KEMET Part Number	Energy (½CVa²) – (½CVd²)	Nominal Capacitance	Maximum DF at 25°C, 120 Hz	Maximum ESR at 25°C, 100 kHz	Maximum DC Leakage at 25°C, Vr, 5 min charge time	Maximum Allowable Ripple Current at 45°C, 100 kHz	MSL	Maximum Operating Temperature
VDC	EIA/mm		mJ	μF	%	mΩ	μА	mA		°C
6.3/5.7	3528/1.2	T520T107M006ATE070	1.2	100	10	70	63.0	1230	3	105
6.3/5.7	3528/1.2	T520T157M006ATE070	1.7	150	10	70	94.5	1230	3	105
16/12.8	3528/1.2	T521T336M016ATE070	2.6	33	10	70	52.8	1230	3	105
6.3/5.7	3528/1.5	T520M157M006ATE070	1.7	150	10	70	94.5	1310	3	105
6.3/5.7	7343/1.5	T545W477M006ATE035	5.4	470	10	35	296.1	2270	3	105
6.3/5.7	7343/1.5	T545W477M006ATE045	5.4	470	10	45	296.1	2000	3	105
6.3/5.7	7343/1.5	T545W477M006ATE055	5.4	470	10	55	296.1	1810	3	105
6.3/5.7	7343/1.5	T548W687M006APE055	7.9	680	10	55	428.4	2680	4	85
16/12.8	7343/1.5	T545W476M016ATE045	3.6	47	10	45	75.2	2000	3	105
16/12.8	7343/1.5	T548W686M016APE050	5.3	68	10	50	108.8	2820	4	85
16/12.8	7343/1.5	T548W107M016APE050	7.7	100	10	50	160.0	2820	4	85
20/16.0	7343/1.5	T545W476M020ATE045	5.8	47	10	45	94.0	2000	3	105
20/16.0	7343/1.5	T545W476M020ATE055	5.8	47	10	55	94.0	1810	3	105
35/28.0	7343/1.5	T548W476M035APE090	18.2	47	10	90	164.5	2100	4	85
10/9.0	7360/1.5	T520J337M010ATE070	9.1	330	10	70	330.0	1700	4	85
16/12.8	7360/1.5	T523J227M016APE070	17.0	220	10	100	352.0	2100	4	85
6.3/5.7	3528/2.0	T520B227M006ATE070	2.5	220	10	70	138.6	1350	3	105
16/12.8	3528/2.0	T521B226M016ATE070	1.7	22	10	70	35.2	1350	3	105
20/16.0	3528/2.0	T521B226M020ATE070	2.7	22	10	70	44.0	1350	3	105
6.3/5.7	7343/2.0	T545V337M006ATE045	3.8	330	10	45	207.9	2040	3	105
6.3/5.7	7343/2.0	T545V477M006ATE055	5.4	470	10	55	296.1	1850	3	105
6.3/5.7	7343/2.0	T548V108M006APE055	11.6	1000	10	55	630.0	2740	4	85
10/9.0	7343/2.0	T545V227M010ATE045	7.9	220	10	45	220.0	2040	3	105
16/12.8	7343/2.0	T545V476M016ATE045	3.6	47	10	45	75.2	2040	3	105
16/12.8	7343/2.0	T545V476M016ATE070	3.6	47	10	70	75.2	1640	3	105
16/12.8	7343/2.0	T545V107M016ATE050	7.7	100	10	50	160.0	1940	3	105
16/12.8	7343/2.0	T548V157M016APE050	11.6	150	10	50	240.0	2870	3	105
20/16.0	7343/2.0	T545V476M020ATE070	5.8	47	10	70	94.0	1640	3	105
6.3/5.7	7360/2.0	T545H108M006ATE055	11.6	1000	20	55	630.0	1850	3	85
6.3/5.7	7360/2.0	T545H158M006ATE035	17.4	1500	20	35	945.0	2320	3	85
6.3/5.7	7360/2.0	T545H158M006ATE055	17.4	1500	20	55	945.0	1850	3	85
10/9.0	7360/2.0	T520H827M010ATE055	29.5	820	10	55	820.0	1910	4	85
16/12.8	7360/2.0	T545H187M016ATE055	13.9	180	20	55	288.0	1910	3	85
16/12.8	7360/2.0	T548H337M016APE070	25.5	330	10	70	528.0	2510	4	85
16/12.8	7360/2.0	T523H477M016APE070	36.4	470	10	70	752.0	2510	4	85
6.3/5.7	7343/4.3	T520X687M006ATE025	7.9	680	10	25	428.4	3150	3	105
10/9.0	7343/4.0	T545Y337M010ATE035	11.9	330	10	35	330.0	2630	3	105
16/12.8	7343/4.3	T545X157M016ATE040	11.6	150	10	40	240.0	2490	3	105
16/12.8	7343/4.3	T545X227M016ATE035	17.0	220	10	35	352.0	2660	3	105
16/12.8	7343/4.3	T545X337M016ATE025	25.5	330	10	25	528.0	3150	3	105
16/12.8	7360/4.3	T5210477M016ATE025	36.4	470	10	25	752.0	3470	3	105
VDC	EIA/mm		mJ	μF	%	mΩ	μА	mA		°C
Rated Voltage/ Application Voltage	Case Size/ Case Height	KEMET Part Number	Energy	Nominal Capacitance	Maximum DF at 25°C, 120 Hz	Maximum ESR at 25°C, 100 kHz	Maximum DC Leakage at 25°C, Vr, 5 min charge time	Maximum Allowable Ripple Current at 45°C, 100 kHz	MSL	Maximum Operating Temp

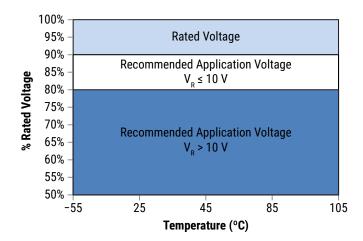
[•] Bold text denotes "Under Development." Engineeiring samples available upon request.

[•] Refer to Ordering Information for additional detail.

[•] Energy = $\frac{1}{2}$ * Nominal Cap * (Application Voltage^2 * Dropout Voltage^2)/1000; a 3 V dropout voltage was used for the calculation.



Derating Guidelines



Voltage Rating	Maximum Recommended Steady State Voltage
	-55°C to 105°C
6.3 V ≤ Vr ≤ 10 V	90% of Vr
10 V < Vr	80% of Vr

 V_R = Rated Voltage

Recommended Application Voltage

KOCAP's are solid state capacitors that demonstrate no wearout mechanism when operated within their recommended guidelines. While the KOCAP can be operated at full rated voltage, most circuit designers seek a minimum level of assurance in long term reliability which should be demonstrated with data. A voltage derating can provide the desired level of demonstrated reliability based on industry accepted acceleration models. Since most applications do require long term reliability, KEMET recommends that designers consider a voltage derating, according the graphic above, for the maximum steady state voltage.



Ripple Current/Ripple Voltage

Permissible AC ripple voltage and current are related to equivalent series resistance (ESR) and the power dissipation capabilities of the device.

Permissible AC ripple voltage which may be applied is limited by two criteria:

- a. The positive peak AC voltage plus the DC bias voltage, if any, must not exceed the DC voltage rating of the capacitor.
- b. The negative peak AC voltage, in combination with bias voltage, if any, must not exceed the allowable limits specified for reverse voltage.

The maximum power dissipation by case size can be determined using the below table.

Temperature Compensation Multipliers for Maximum Ripple Current									
T ≤ 45°C	45° C < T ≤ 85°C	85°C < T ≤ 105°C							
1.00	0.70	0.25							

T= Environmental Temperature

Using the P max of the device, the maximum allowable rms ripple current or voltage may be determined.

 $I(max) = \sqrt{P \max/R}$ $E(max) = Z \sqrt{P \max/R}$

I = rms ripple current (amperes)

E = rms ripple voltage (volts)

P max = maximum power dissipation(watts)

R = ESR at specified frequency (ohms)

Z = Impedance at specified frequency (Ohms)

Refer to part number listings for permittable Arms limits.

Case Code	EIA Case Code	Maximum Power Dissipation (P max) mWatts at 45°C with +30°C Rise						
		For T520/T521/T545	For T523/T548					
Т	3528-12	105	N/A					
М	3528-15	120	N/A					
В	3528-21	127	N/A					
W	7343-15	180	395					
V	7343-20	187	410					
Υ	7343-40	241	N/A					
Х	7343-43	247	N/A					
J	7360-15	200	440					
Н	7360-20	200	440					
0	7360-43	300	N/A					

The maximum power dissipation rating must be reduced with increasing environmental operating temperatures. Refer to the Temperature Compensation Multiplier table for details.



Surge Voltage

Surge voltage is the maximum voltage (peak value) which may be applied to the capacitor. The surge voltage must not be applied for periodic charging and discharging in the course of normal operation and cannot be part of the application voltage. Surge voltage capability is demonstrated by application of 1,000 cycles at operating temperature. The parts are charged through a 33 Ohm resistor for 30 seconds and then discharged though a 33 Ohm resistor for each cycle.

Rated Voltage (V)	Surge Voltage (V)						
−55°C to 105°C							
2.5	3.3						
6.3	8.3						
10	13.2						
16	21.1						
20	26.4						
25	33.0						
35	46.2						

Reverse Voltage

Polymer electrolytic capacitors are polar devices and may be permanently damaged or destroyed if connected in the wrong polarity. These devices will withstand a small degree of transient voltage reversal for short periods as shown in the below table.

Temperature	Permissible Transient Reverse Voltage
25°C	15% of Rated Voltage
55°C	10% of Rated Voltage
85°C	5% of Rated Voltage
105°C	3% of Rated Voltage
125°C*	1% of Rated Voltage

^{*}For series rated to 125°C



Table 2 - Land Dimensions/Courtyard

For T520/T521/T545

KEMET	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)				N	ledian	sity Lev (Nomin	al) Lan	ıd	Density Level C: Minimum (Least) Land Protrusion (mm)					
Case	EIA	W	L	S	V 1	V2	W	L	S	V 1	V2	W	L	S	V1	V2
Т	3528-12	2.35	2.21	0.92	6.32	4.00	2.23	1.80	1.12	5.22	3.50	2.13	1.42	1.28	4.36	3.24
В	3528-21	2.35	2.21	0.92	6.32	4.00	2.23	1.80	1.12	5.22	3.50	2.13	1.42	1.28	4.36	3.24
М	3528-15	2.35	2.21	0.92	6.32	4.00	2.23	1.80	1.12	5.22	3.50	2.13	1.42	1.28	4.36	3.24
W	7343-15	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84
V	7343-20	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84
Υ1	7343-40	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84
Х1	7343-43	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84
J	7360-15	4.25	2.77	3.67	10.22	7.30	4.13	2.37	3.87	9.12	6.80	4.03	1.99	4.03	8.26	6.54
Н	7360-20	4.25	2.77	3.67	10.22	7.30	4.13	2.37	3.87	9.12	6.80	4.03	1.99	4.03	8.26	6.54
01	7360-43	4.25	2.77	3.67	10.22	7.30	4.13	2.37	3.87	9.12	6.80	4.03	1.99	4.03	8.26	6.54

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component desity product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC standard 7351 (IPC-7351).

¹ Height of these chips may create problems in wave soldering.

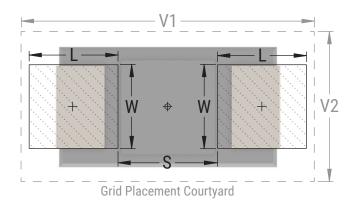




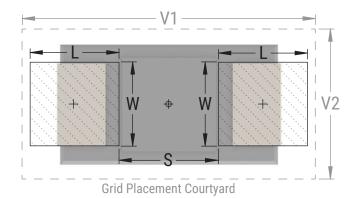
Table 2 - Land Dimensions/Courtyard cont'd

For T523/T548

KEMET	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)			Density Level B: Median (Nominal) Land Protrusion (mm)				Density Level C: Minimum (Least) Land Protrusion (mm)							
Case	EIA	W	L	S	V 1	V2	W	L	S	V1	V2	W	L	S	V 1	V2
W	7343-15	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84
٧	7343-20	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84
J	7360-15	4.60	3.07	3.07	10.22	7.30	4.48	2.67	3.27	9.12	6.80	4.38	2.29	3.43	8.26	6.54
Н	7360-20	4.60	3.07	3.07	10.22	7.30	4.48	2.67	3.27	9.12	6.80	4.38	2.29	3.43	8.26	6.54

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. Density Level C: For high component desity product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC standard 7351 (IPC-7351).





Soldering Process

KEMET's families of surface mount capacitors are compatible with wave (single or dual), convection, IR, or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020D standard for moisture sensitivity testing. The devices can safely withstand a maximum of three reflow passes at these conditions.

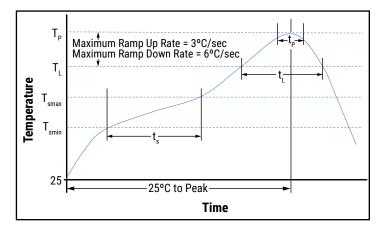
Please note that although the X/7343-43 and O/7360-43 case size can withstand wave soldering, the tall profile (4.3 mm maximum) dictates care in wave process development.

Hand soldering should be performed with care due to the difficulty in process control. If performed, care should be taken to avoid contact of the soldering iron to the molded case. The iron should be used to heat the solder pad, applying solder between the pad and the termination, until reflow occurs. Once reflow occurs, the iron should be removed immediately. "Wiping" the edges of a chip and heating the top surface is not recommended.

Profile Feature	Pb-Free Assembly
Preheat/Soak	
Temperature Minimum (T _{Smin})	150°C
Temperature Maximum (T _{Smax})	200°C
Time (t_s) from T_{smin} to T_{smax})	60 – 120 seconds
Ramp-up Rate (T _L to T _P)	3°C/seconds maximum
Liquidous Temperature (T _L)	217°C
Time Above Liquidous (t _L)	60 - 150 seconds
Peak Temperature (T _P)	250°C* 260°C**
Time within 5°C of Maximum Peak Temperature (t _p)	30 seconds maximum
Ramp-down Rate $(T_p \text{ to } T_L)$	6°C/seconds maximum
Time 25°C to Peak Temperature	8 minutes maximum

Note: All temperatures refer to the center of the package, measured on the package body surface that is facing up during assembly reflow.

^{**}Case Size B, H, J, M, T, V, and W



Storage

All KO-Cap Series are shipped in moisture barrier bags (MBBs) with desiccant and humidity indicator card (HIC). These parts are classified as MSL3 (Moisture Sensitivity Level 3) or MSL4 (Moisture Sensitivity Level 4) per IPC/JEDEC J-STD-020 and packaged per IPC/JEDEC J-STD-033. Refer to Table 1 for part type specification. MSL3 specifies a floor time of 168H at 30°C maximum temperature and 60% relative humidity. MSL4 specifies a floor time of 72H at 30°C maximum temperature and 60% relative humidity. Unused capacitors should be sealed in a MBB with fresh desiccant.

Calculated shelf life in sealed bag:

- 12 months from bag seal date in a storage environment of < 40°C and humidity < 90% RH
- 24 months from bag seal date in a storage environment of < 30°C and humidity < 70% RH

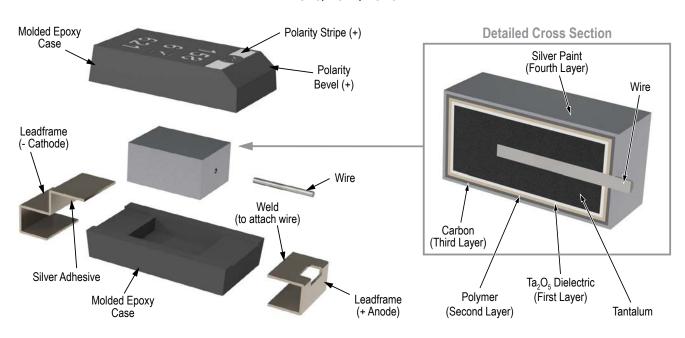
If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure

^{*}Case Size O, Y, and X

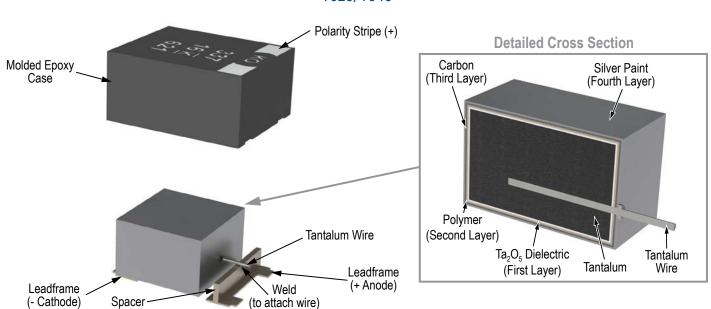


Construction

T520/T521/T545

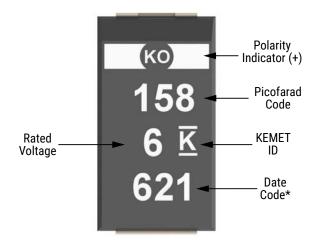


T523/T548





Capacitor Marking



* 621 = 21ST week of 2016

Date Code *								
1st digit = Last number of Year	2 = 2012							
	3 = 2013							
	4 = 2014							
	5 = 2015							
	6 = 2016							
	7 = 2017							
2 nd and 3 rd digit = Week of the Year	01 = 1 st week of the Year to 52 = 52 nd week of the Year							



Tape & Reel Packaging Information

KEMET's molded chip capacitor families are packaged in 8 and 12 mm plastic tape on 7" and 13" reels in accordance with EIA Standard 481: Embossed Carrier Taping of Surface Mount Components for Automatic Handling. This packaging system is compatible with all tape-fed automatic pick-and-place systems.

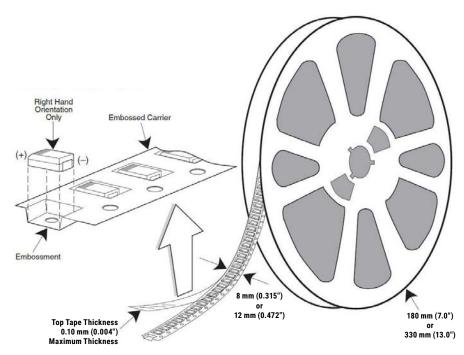


Table 3 - Packaging Quantity

Case	Code	Tape Width (mm)	7" Reel*	13" Reel*	
KEMET	EIA				
T	3528-12	8	2,500	10,000	
М	3528-15	8	2,000	8,000	
В	3528-21	8	2,000	8,000	
W	7343-15	12	1,000	3,000	
V	7343-20	12	1,000	3,000	
Υ	7343-40	12	500	2,000	
Х	7343-43	12	500	2,000	
J	7360-15	12	1,000	3,000	
Н	7360-20	12	1,000	3,000	
0	7360-43	12	500	2,000	

^{*} No C-Spec required for 7" reel packaging. C-7280 required for 13" reel packaging.



Figure 1 - Embossed (Plastic) Carrier Tape Dimensions

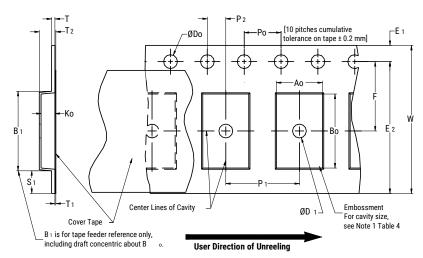


Table 4 - Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)										
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum	
8 mm		1.0 (0.039)			2.0±0.05	25.0 (0.984)				
12 mm	1.5 +0.10/-0.0 (0.059+0.004/-0.0)	1.5	1.75±0.10 (0.069±0.004)	4.0±0.10 (0.157±0.004)	(0.079±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)	
16 mm	(0.002 - 0.00 1/ 0.0)	(0.059)	(3.337_0.001)	(33. 20.00 1)	2.0±0.1 (0.079±0.059)	(1.181)	(3.321)	(3.321)	(3.301)	

Variable Dimensions — Millimeters (Inches)									
Tape Size	Pitch	B ₁ Maximum Note 4	E ₂ Minimum	F	P ₁	T ₂ Maximum	W Maximum	A ₀ , B ₀ & K ₀	
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5±0.05 (0.138±0.002)	2.0±0.05 or 4.0±0.10 (0.079±0.002 or 0.157±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5±0.05 (0.217±0.002)	2.0±0.05 (0.079±0.002) or 4.0±0.10 (0.157±0.004) or 8.0±0.10 (0.315±0.004)	4.6 (0.181)	12.3 (0.484)	Note 5	
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5±0.10 (0.295±0.004)	4.0±0.10 (0.157±0.004) to 12.0±0.10 (0.472±0.004)	8.0 (0.315)	16.3 (0.642)		

- 1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
- 2. The tape, with or without components, shall pass around R without damage (see Figure 4).
- 3. If S, < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481–D, paragraph 4.3, section b).
- 4. B_1 dimension is a reference dimension for tape feeder clearance only.
- 5. The cavity defined by A_{o} , B_{o} and K_{o} shall surround the component with sufficient clearance that:
 - (a) the component does not protrude above the top surface of the carrier tape.
 - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - (c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 2).
 - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 mm and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 3).
 - (e) see Addendum in EIA Standard 481-D for standards relating to more precise taping requirements.



Packaging Information Performance Notes

1. Cover Tape Break Force: 1.0 Kg minimum.

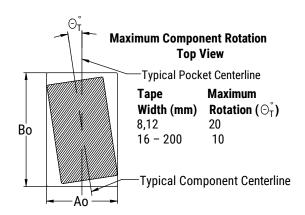
2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 Newton (10 to 100 gf)
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ±10 mm/minute.

3. Labeling: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards 556 and 624.*

Figure 2 - Maximum Component Rotation



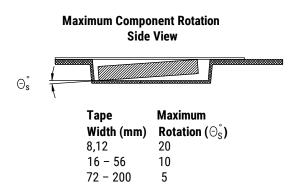


Figure 3 - Maximum Lateral Movement

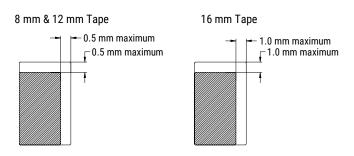


Figure 4 - Bending Radius

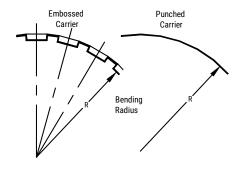
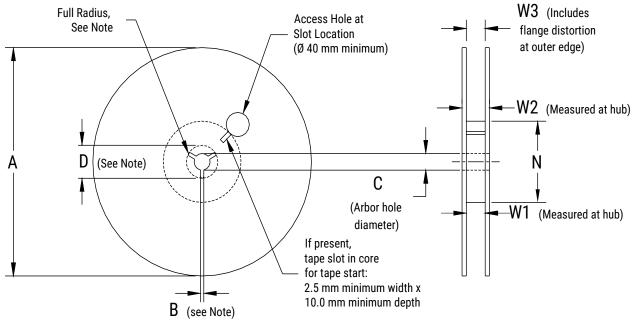




Figure 5 - Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 5 - Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)										
Tape Size	A	B Minimum	С	D Minimum						
8 mm	178±0.20									
12 mm	(7.008±0.008) or	1.5 (0.059)	13.0+0.5/-0.2 (0.521+0.02/-0.008)	20.2 (0.795)						
16 mm	330±0.20 (13.000±0.008)	(0.005)	(0.02110.02)	(0.733)						
	Variable Dimensions — Millimeters (Inches)									
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃						
8 mm		8.4+1.5/-0.0 (0.331+0.059/-0.0)	14.4 (0.567)							
12 mm	50 (1.969)	12.4+2.0/-0.0 (0.488+0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference						
16 mm		16.4+2.0/-0.0 (0.646+0.078/-0.0)	22.4 (0.882)							



Figure 6 - Tape Leader & Trailer Dimensions

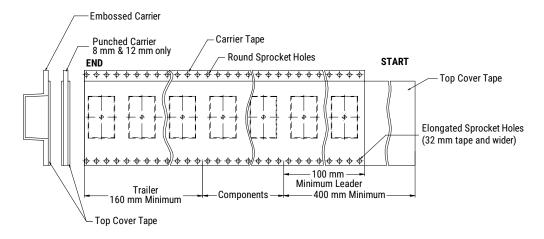
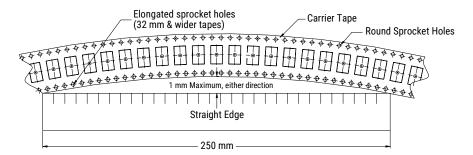


Figure 7 – Maximum Camber





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