

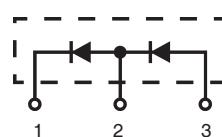
Phase-leg Rectifier Diode

ISOPLUS220™

Electrically Isolated Back Surface

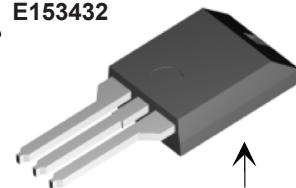
V _{RSM}	V _{RRM}	Type
V	V	
900	800	DSP 8-08AC
1300	1200	DSP 8-12AC

Preliminary Data Sheet



V_{RRM} = 800/1200 V
I_{F(AV)M} = 2 x 11 A

ISOPLUS 220™
E153432



Isolated back surface*

Symbol	Test Conditions	Maximum Ratings		
I _{FRMS}	T _{VJ} = T _{VJM}	30	A	
I _{F(AV)M}	T _{case} = 100°C; 180° sine	2 x 11	A	
I _{FSM}	T _{VJ} = 45°C; t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	100	A	
	T _{VJ} = 150°C; t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	105	A	
I ² t	T _{VJ} = 45°C t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	85	A	
	T _{VJ} = 150°C; t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	90	A	
		50	A ² s	
		45	A ² s	
		35	A ² s	
		30	A ² s	
T _{VJ}		-40...+150	°C	
T _{VJM}		150	°C	
T _{stg}		-55...+150	°C	
T _L	1.6 mm (0.063 in) from case for 10 s	260	°C	
V _{ISOL}	50/60 Hz RMS; I _{ISOL} ≤ 1 mA	2500	V~	
F _c	Mounting Force	11...65 / 2.5..15	N/lb	
Weight		2	g	

Symbol	Test Conditions	Characteristic Values		
I _R ^①	V _R = V _{RRM} ; T _{VJ} = 25°C T _{VJ} = 150°C	≤ 10	μA	
		≤ 0.7	mA	
V _F ^②	I _F = 10 A; T _{VJ} = 25°C T _{VJ} = 125°C	≤ 1.22	V	
		≤ 1.26	V	
V _{T0}	For power-loss calculations only	0.8	V	
r _T	T _{VJ} = T _{VJM}	41	mΩ	
R _{thJC}	DC current	1.8	K/W	
R _{thCK}	DC current (with heatsink compound)	typ. 0.6	K/W	
a	Maximum allowable acceleration	100	m/s ²	

Notes: Data given for T_{VJ} = 25°C and per diode unless otherwise specified

① Pulse test: pulse Width = 5 ms, Duty Cycle < 2.0 %

② Pulse test: pulse Width = 300 μs, Duty Cycle < 2.0 %

IXYS reserves the right to change limits, test conditions and dimensions.

