

### FEATURES

**Input voltage range: 2.5 V to 5.5 V**  
**Output current range: 0 mA to 150 mA**  
**Output voltage accuracy:  $\pm 1\%$**   
**Operating temperature range:  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$**

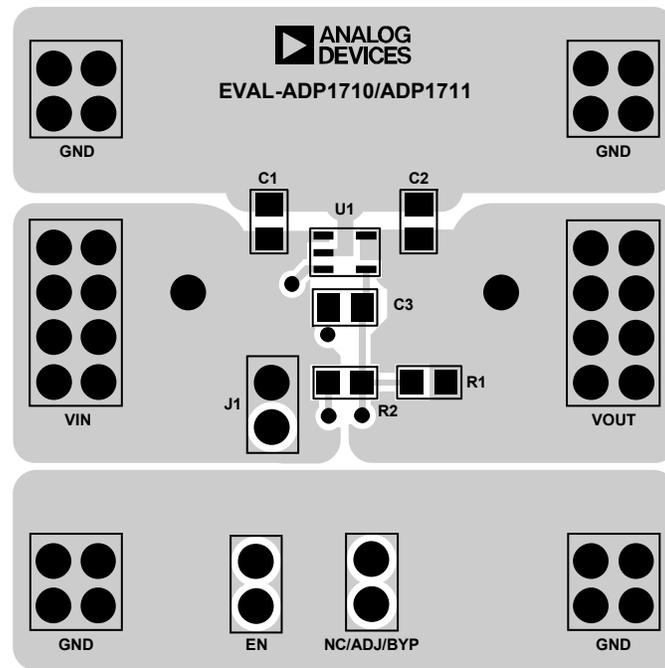
### GENERAL DESCRIPTION

The ADP1710/ADP1711 evaluation boards can be used to demonstrate the functionality of the ADP1710 and ADP1711 series of linear regulators. Depending on what component combinations are used, the evaluation board can be used for ADP1710 fixed output voltage devices, ADP1710 adjustable output voltage devices, and ADP1711 fixed output voltage devices with a bypass capacitor.

Simple device measurements such as line and load regulation, dropout, and ground current can be demonstrated with just a single voltage supply, a voltage meter, a current meter, and load resistors.

For more details about the [ADP1710](#) and [ADP1711](#) linear regulators, visit [www.analog.com](http://www.analog.com).

### EVALUATION BOARD



NC = NO CONNECT

06311-001

Figure 1.

### Rev. 0

Evaluation boards are only intended for device evaluation and not for production purposes. Evaluation boards as supplied "as is" and without warranties of any kind, express, implied, or statutory including, but not limited to, any implied warranty of merchantability or fitness for a particular purpose. No license is granted by implication or otherwise under any patents or other intellectual property by application or use of evaluation boards. Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Analog Devices reserves the right to change devices or specifications at any time without notice. Trademarks and registered trademarks are the property of their respective owners. Evaluation boards are not authorized to be used in life support devices or systems.

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## REVISION HISTORY

3/07—Revision 0: Initial Version

# EVALUATION BOARD HARDWARE AND SCHEMATIC

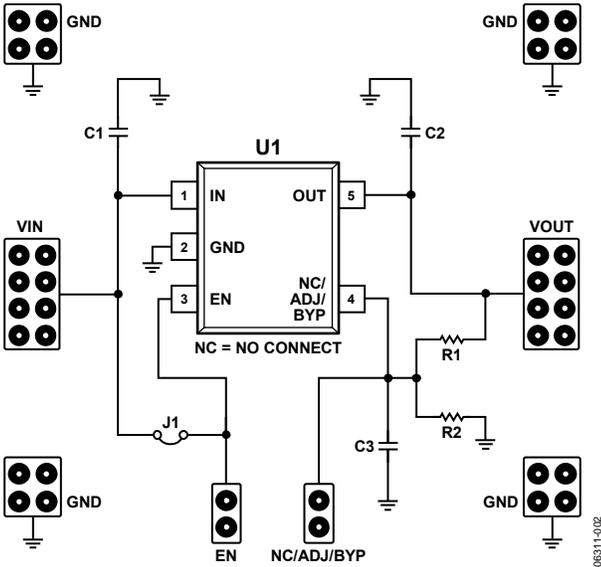


Figure 2. Evaluation Board Schematic

Table 1. Evaluation Board Hardware Components

Component	Function	Description
U1 <sup>1</sup>	Linear regulator	ADP1710/ADP1711 Low Dropout Linear Regulator.
C1	Input capacitor	1 $\mu$ F Input Bypass Capacitor.
C2	Output capacitor	1 $\mu$ F Output Capacitor. Required for stability and transient performance.
C3 <sup>2</sup>	Bypass capacitor	10 nF Bypass Capacitor.
R1, R2 <sup>3</sup>	Output voltage adjust resistors	External Resistors. Used to set VOUT with the adjustable version of ADP1710.
J1	Jumper	Jumper. Connects EN to VIN for automatic startup.

<sup>1</sup> Component varies depending on which evaluation board type is ordered.  
<sup>2</sup> Component present only on boards with ADP1711 fixed output devices.  
<sup>3</sup> Components present only on boards with ADP1710 adjustable output devices.

# EVAL-ADP1710/ADP1711

## EVALUATION BOARD CONFIGURATIONS

The ADP1710/ADP1711 evaluation boards come supplied with different components depending on which version is ordered. Components common to all versions are C1, C2, and J1.

### ADP1710 ADJUSTABLE OUTPUT

Evaluation boards that come supplied with ADP1710 adjustable output voltage option devices have the additional R1 and R2 resistors included. Figure 3 shows the schematic of this evaluation board configuration.

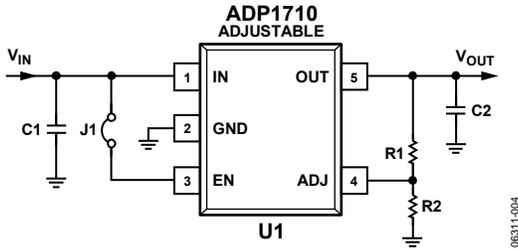


Figure 3. ADP1710 Adjustable Output

The output voltage is set based on the following equation:

$$V_{OUT} = 0.8 \text{ V} (1 + R1/R2)$$

For example, if R1 = 10 kΩ and R2 = 4.99 kΩ, then V<sub>OUT</sub> is 2.403 V.

### ADP1711 FIXED OUTPUT WITH BYPASS CAPACITOR

Evaluation boards that come supplied with ADP1711 fixed output voltage option devices with a bypass capacitor have C3 populated. Figure 4 shows the schematic of this evaluation board configuration.

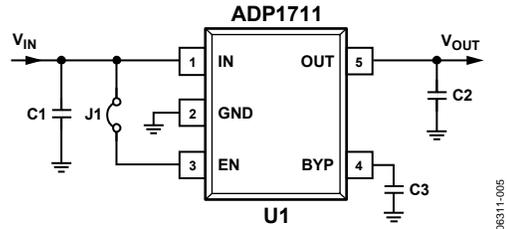


Figure 4. ADP1711 Fixed Output with Bypass Capacitor

## OUTPUT VOLTAGE MEASUREMENTS

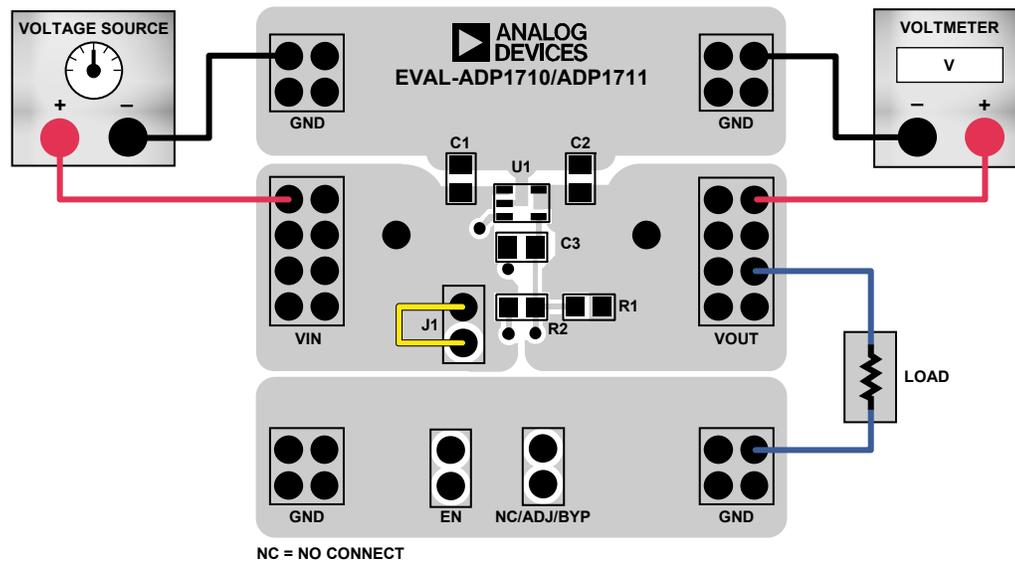


Figure 5.

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Figure 5 shows how the evaluation board can be connected to a voltage source and a voltage meter for some basic output voltage accuracy measurements. A resistor can be used as the load for the regulator. Ensure the resistor has a power rating adequate to handle the power expected to be dissipated across it. An electronic load can be used as an alternative. Ensure the voltage source used can supply adequate current for the expected load levels.

Follow these steps to connect to a voltage source and voltage meter:

1. Connect the negative terminal (–) of the voltage source to one of the GND pads on the evaluation board.
2. Connect the positive terminal (+) of the voltage source to the VIN pad of the evaluation board.
3. Connect a load between the VOUT pad and one of the GND pads.
4. Connect the negative terminal (–) of the voltage meter to one of the GND pads, and the positive terminal (+) of the voltage meter to the VOUT pad.
5. The voltage source can now be turned on. If J1 is inserted (this connects EN to VIN for automatic startup), then the regulator powers up.

If large load currents are used, then the user must connect the voltage meter as close as possible to the output capacitor to reduce the effects of IR drops.

### LINE REGULATION

For line regulation measurements, the regulator's output is monitored while its input is varied. For good line regulation, the output must change as little as possible with varying input levels. To ensure the device is not in dropout during this measurement,  $V_{IN}$  must be varied between  $V_{OUT_{NOM}} + 0.5\text{ V}$  (or  $2.5\text{ V}$ , whichever is greater) to  $V_{IN_{MAX}}$ . For an ADP1710 with  $3.3\text{ V}$  output, this means  $V_{IN}$  needs to be varied between  $3.8\text{ V}$  and  $5.5\text{ V}$ . This measurement can be repeated under different load conditions. Figure 6 shows the typical line regulation performance of an ADP1710 with  $3.3\text{ V}$  output.

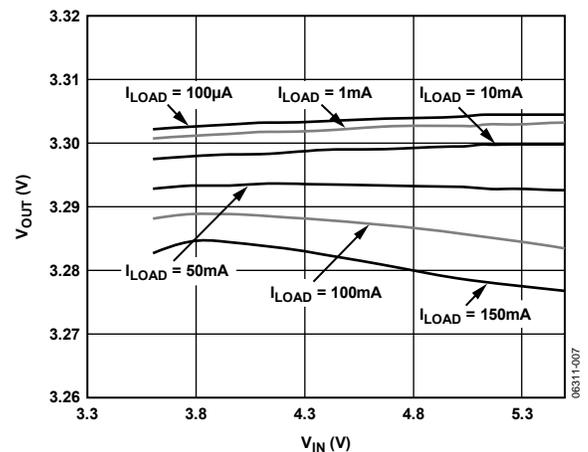


Figure 6. ADP1710 Line Regulation

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# EVAL-ADP1710/ADP1711

## LOAD REGULATION

For load regulation measurements, the regulator's output is monitored while the load is varied. For good load regulation, the output must change as little as possible with varying load levels. The input voltage must be held constant during this measurement. The load current can be varied from 0 mA to 150 mA. Figure 7 shows the typical load regulation performance of an ADP1710 with 3.3 V output for an input voltage of 3.8 V.

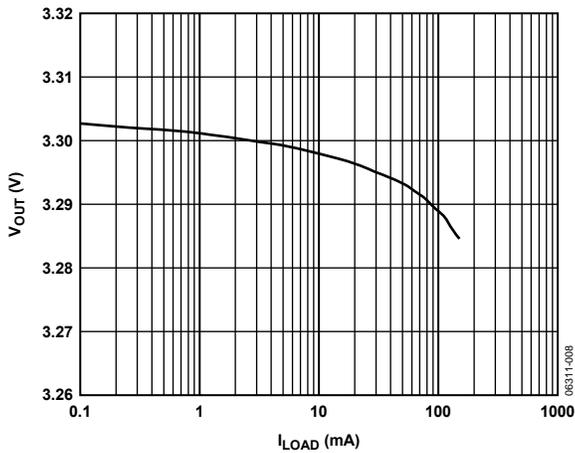


Figure 7. ADP1710 Load Regulation

## DROPOUT VOLTAGE

Dropout voltage measurements can also be performed using the configuration shown in Figure 5. Dropout voltage is defined as the input-to-output voltage differential when the input voltage is set to the nominal output voltage. This applies only for output voltages above 2.5 V. Dropout voltage increases with larger loads. For more accurate measurements, a second voltage meter can be used to monitor the input voltage across the input capacitor. The input supply voltage may need to be adjusted to account for IR drops, especially if large load currents are used. Figure 8 shows a typical curve of dropout voltage measurements with different load currents.

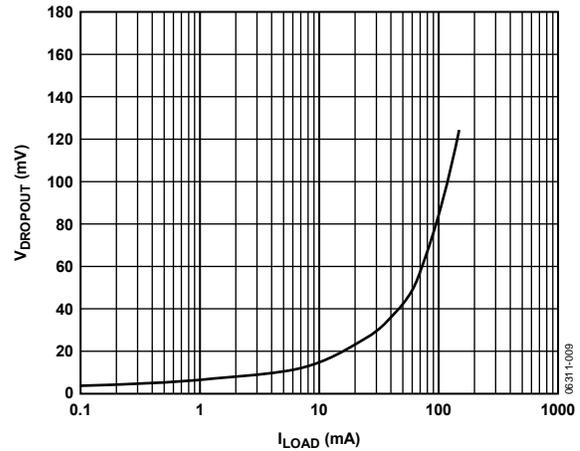


Figure 8. Dropout Voltage vs. Load Current

## GROUND CURRENT MEASUREMENTS

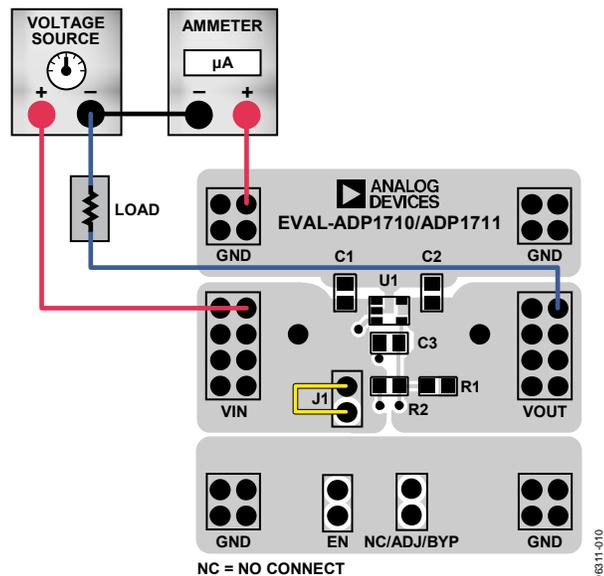


Figure 9.

Figure 9 shows how the evaluation board can be connected to a voltage source and a current meter for ground current measurements. A resistor can be used as the load for the regulator. Ensure the resistor has a power rating adequate to handle the power expected to be dissipated across it. An electronic load can be used as an alternative. Ensure the voltage source used can supply adequate current for the expected load levels.

Follow these steps to connect to a voltage source and current meter:

1. Connect the positive terminal (+) of the voltage source to the VIN pad on the evaluation board.
2. Connect the positive terminal (+) of the current meter to one of the GND pads of the evaluation board.
3. Connect the negative terminal (-) of the current meter to the negative (-) terminal of the voltage source.
4. Connect a load between the VOUT pad of the evaluation board and the negative (-) terminal of the voltage source.
5. The voltage source can now be turned on. If J1 is inserted (this connects EN to VIN for automatic startup), then the regulator powers up.

### GROUND CURRENT CONSUMPTION

Ground current measurements are a way of determining how much current the regulator's internal circuits are consuming, while performing the regulation function. To be efficient, the regulator must consume as little current as possible. Typically, the regulator consumes maximum current when supplying its

largest load level (150 mA). Figure 10 shows the typical ground current consumption for various load levels.

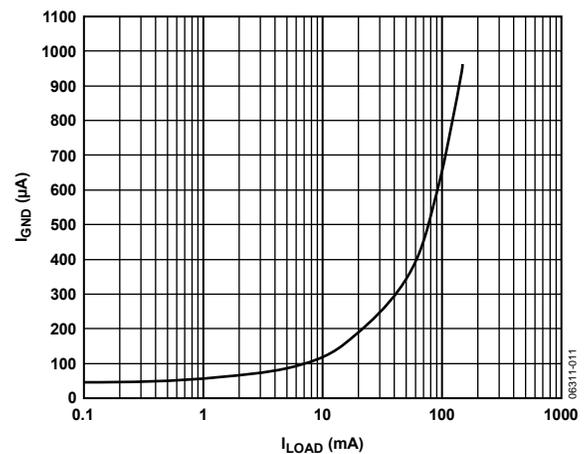


Figure 10. Ground Current vs. Load Current.

When the device is disabled ( $EN = 0 V$ ), ground current is reduced to less than 1  $\mu A$ .

Note: Larger ground current levels are observed when using an evaluation board with R1 and R2 present.

# EVAL-ADP1710/ADP1711

## ORDERING INFORMATION

### BILL OF MATERIALS

Table 2.

Qty	Reference Designator	Description	Manufacturer/Vendor	Vendor Part No.
2	C1, C2	Capacitor, MLCC, 1 $\mu$ F, 16 V, 0805, X7R	Murata or equivalent	GRM21BR71C105KA01
1	C3	Capacitor, MLCC, 10 nF, 100 V, 0805, X7R	Murata or equivalent	GRM21BR72A103KA01
1	R1	Resistor, 10 k $\Omega$ , 1%, 0805	Vishay Dale or equivalent	CRCW080510K0FKEA
1	R2	Resistor, 4.99 k $\Omega$ , 1%, 0805	Vishay Dale or equivalent	CRCW08054K990FKEA
1	J1	Header, single, STR, 2 pins	Digi-Key, Corp.	S1012E-36-ND
1	U1	ADP1710/ADP1711 IC, LDO Regulator	Analog Devices, Inc.	ADP1710AUJZ-R7 ADP1711AUJZ-2.5-R7

### ORDERING GUIDE

Model	Description
ADP1710-EVALZ <sup>1</sup>	Evaluation Board with Adjustable Output ADP1710
ADP1711-2.5-EVALZ <sup>1</sup>	Evaluation Board with 2.5 V Fixed Output ADP1711

<sup>1</sup> Z = RoHS Compliant Part.

### ESD CAUTION



**ESD (electrostatic discharge) sensitive device.** Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.