

30V DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DS}	R _{DS(ON)} Max	Package	I _D T _A = +25°C
-30V	$25m\Omega @V_{GS} = -10V$	SO-8	-6.0 A
-307	$38m\Omega @V_{GS} = -4.5V$	30-0	-4.7A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

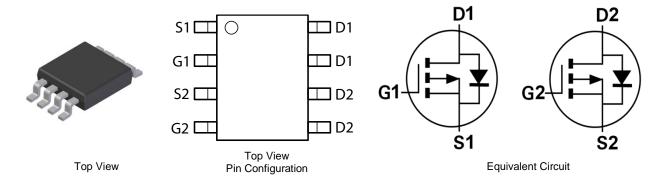
- DC-DC Converters
- Power Management Functions
- Load Switch

Features

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (a)
- Weight: 0.074 grams (approximate)



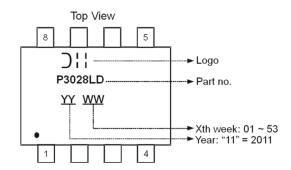
Ordering Information (Note 4)

- 7				
	Part Number	Case	Packaging	
	DMP3028LSD-13	SO-8	2,500/Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information



June 2013

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	-30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Dusin Courset (Nato 5) \	Steady State	T _A = +25°C T _A = +70°C	I _D	-6 -4.7	А
Continuous Drain Current (Note 5) V _{GS} = 10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	-7.4 -5.8	А
Maximum Body Diode Forward Current (Note 6)			Is	-2.5	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-30	Α

Thermal Characteristics

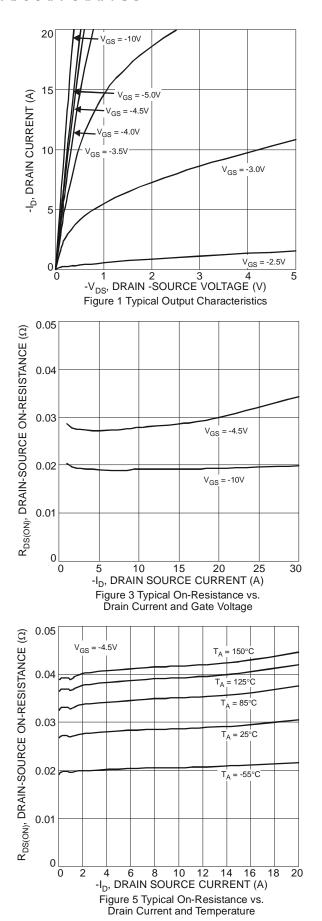
Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	T _A = +25°C	D	1.3	W
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P_{D}	0.8	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	Da	102	°C/W
L Thermal Resistance, Junction to Ambient (Note 3)	t<10s	$R_{\theta JA}$	61	
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	D-	1.7	W
Total Fower Dissipation (Note o)	$T_A = +70^{\circ}C$	P_{D}	1.1	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	Davi	75	°C/W
Internal Resistance, Junction to Ambient (Note 6)	t<10s	R _{0JA}	50	
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	14.5		
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

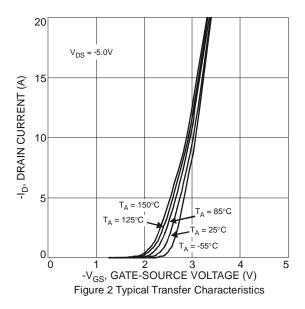
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

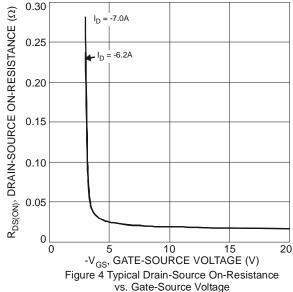
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	-1	_	-3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		_	20	25	~ 0	$V_{GS} = -10V, I_{D} = -7A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}	_	29	38	mΩ	$V_{GS} = -4.5V, I_D = -5.5A$	
Forward Transfer Admittance	Y _{fs}	_	11	_	S	$V_{DS} = -5V, I_{D} = -7A$	
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = -2.1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	1241	_		V _{DS} = -15V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	147	_	pF		
Reverse Transfer Capacitance	Crss	_	110	_		I = 1.0MHZ	
Gate Resistance	R_{G}	_	15	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	22	_		V _{DS} = -15V, I _D = -7A	
Total Gate Charge (V _{GS} = -10V)	Qg	_	10.9	_	nC		
Gate-Source Charge	Q _{gs}	_	3.5	_	IIC		
Gate-Drain Charge	Q _{gd}	_	4.7	_			
Turn-On Delay Time	t _{D(on)}	_	9.7	_			
Turn-On Rise Time	t _r	_	17.1		nS	$V_{GS} = -10V, V_{DD} = -15V, R_{GEN} = 6\Omega,$	
Turn-Off Delay Time	t _{D(off)}	_	60.5	_	113	$I_D = -7A$	
Turn-Off Fall Time	t _f	_	40.4	_			

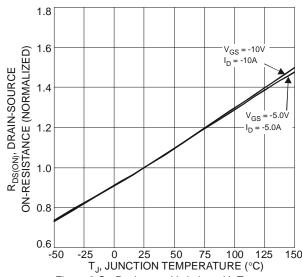
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:



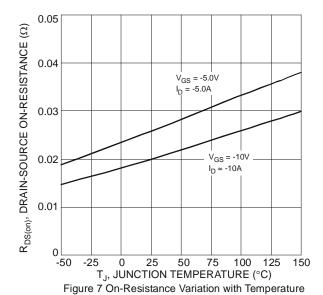


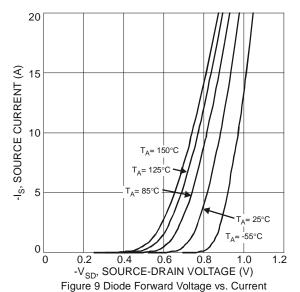












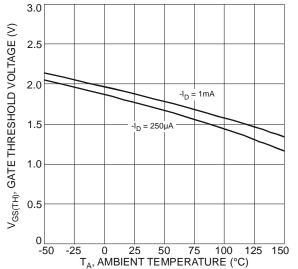
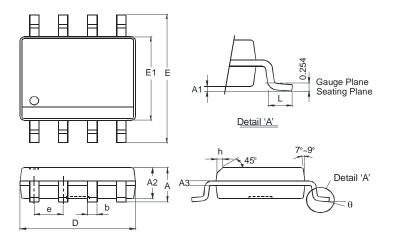


Figure 8 Gate Threshold Variation vs. Ambient Temperature



Package Outline Dimensions

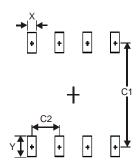
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SO-8					
Dim	Min	Max			
Α	-	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	1.27 Typ				
h	ı	0.35			
L	0.62	0.82			
Θ	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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