

3677 WIRING SIDE

VECTOR DIP PLUGBORDTM
PATTERN 0.042" x 0.10" SPACED HOLES
LA2-P6 LAYOUT PAPER



Pin Position

á

adjacent circuitry

and

Buisn

them could cause shorting.

may have

hol

having

pads.

blug cont ds. Holes

contact area on eit oles without pads

either

side

9

Plugboard, L insufficient

clearance

those nce to

only

CAUTION:

=

Zone Letters A,B,C, etc., on Y axis and X,Y,Z on X axis mark position for 14- or 16-pin DIP's

> the conductor. Before clearance pressing between terminais the widest Olui board, part 9 position terminal (rotate) te) terminals nearest 7 maximize adjacent

2. Wh solder leads, Where blockage, In some ca tin-coat cases, This is usua a soldering usually exists, a s non yen small percentage t "skin" easily pe be required age of the h holes d by c component may have

 Intended for use in non-hostile environments up to 200 volts RMS or 3 volts DC.

NOTES: