

April 1988 Revised September 2000

74F138

1-of-8 Decoder/Demultiplexer

General Description

The F138 is a high-speed 1-of-8 decoder/demultiplexer. This device is ideally suited for high-speed bipolar memory chip select address decoding. The multiple input enables allow parallel expansion to a 1-of-24 decoder using just three F138 devices or a 1-of-32 decoder using four F138 devices and one inverter.

Features

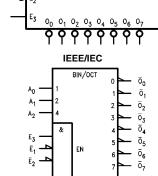
- Demultiplexing capability
- Multiple input enable for easy expansion
- Active LOW mutually exclusive outputs

Ordering Code:

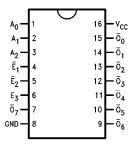
Order Number	Package Number	Package Description
74F138SC	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
74F138SJ	M16D	16-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74F138PC	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Logic Symbols



Connection Diagram



Unit Loading/Fan Out

Pin Names	Description.	U.L.	Input I _{IH} /I _{IL}	
	Description	HIGH/LOW	Output I _{OH} /I _{OL}	
A ₀ -A ₂	Address Inputs	1.0/1.0	20 μA/–0.6 mA	
\overline{E}_1 , \overline{E}_2	Enable Inputs (Active LOW)	1.0/1.0	20 μA/–0.6 mA	
E ₃	Enable Input (Active HIGH)	1.0/1.0	20 μA/–0.6 mA	
$\overline{O}_0 - \overline{O}_7$	Outputs (Active LOW)	50/33.3	−1 mA/20 mA	

Truth Table

Inputs				Outputs									
E ₁	E ₂	E ₃	A ₀	A ₁	A ₂	O ₀	<u>O</u> 1	O ₂	<u>O</u> 3	O ₄	<u>O</u> 5	<u>О</u> 6	07
Н	Х	Χ	Х	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н
X	Н	Χ	Х	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н
X	X	L	Χ	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н
L	L	Н	L	L	L	L	Н	Н	Н	Н	Н	Н	Н
L	L	Н	Н	L	L	Н	L	Н	Н	Н	Н	Н	Н
L	L	Н	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н
L	L	Н	Н	Н	L	Н	Н	Н	L	Н	Н	Н	Н
L	L	Н	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н
L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н
L	L	Н	L	Н	Н	Н	Н	Н	Н	Н	Н	L	Н
L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L

H = HIGH Voltage Level

Functional Description

The F138 high-speed 1-of-8 decoder/demultiplexer accepts three binary weighted inputs (A_{0,} A_{1,} A₂) and, when enabled, provides eight mutually exclusive active LOW outputs $(\overline{O_0} - \overline{O_7})$. The F138 features three Enable inputs, two active LOW (\overline{E}_1 , \overline{E}_2) and one active HIGH (E_3). All outputs will be HIGH unless \overline{E}_1 and \overline{E}_2 are LOW and E_3 is HIGH. This multiple enable function allows easy parallel

expansion of the device to a 1-of-32 (5 lines to 32 lines) decoder with just four F138 devices and one inverter (See Figure 1). The F138 can be used as an 8-output demultiplexer by using one of the active LOW Enable inputs as the data input and the other Enable inputs as strobes. The Enable inputs which are not used must be permanently tied to their appropriate active HIGH or active LOW state.

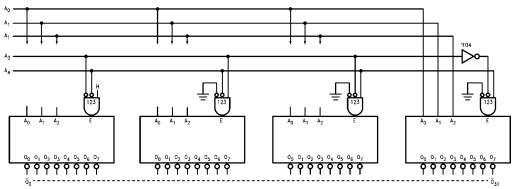
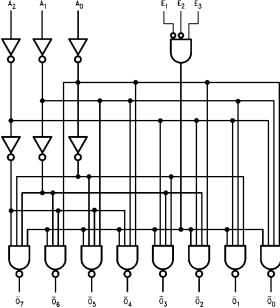


FIGURE 1. Expansion to 1-of-32 Decoding

L = LOW Voltage Level X = Immaterial

Logic Diagram



 $\overline{0}_7 \quad \overline{0}_6 \quad \overline{0}_5 \quad \overline{0}_4 \quad \overline{0}_3 \quad \overline{0}_2 \quad \overline{0}_1 \quad \overline{0}_0$ Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

Absolute Maximum Ratings(Note 1)

 $\begin{array}{ll} \mbox{Storage Temperature} & -65^{\circ}\mbox{C to } +150^{\circ}\mbox{C} \\ \mbox{Ambient Temperature under Bias} & -55^{\circ}\mbox{C to } +125^{\circ}\mbox{C} \\ \end{array}$

Junction Temperature under Bias -55° C to $+150^{\circ}$ C V_{CC} Pin Potential to Ground Pin -0.5V to +7.0V Input Voltage (Note 2) -0.5V to +7.0V

Input Current (Note 2)

-0.5v to +7.0v

-30 mA to +5.0 mA

Voltage Applied to Output in HIGH State (with $V_{CC} = 0V$)

 $\begin{array}{ll} \mbox{Standard Output} & -0.5\mbox{V to V}_{\mbox{CC}} \\ \mbox{3-STATE Output} & -0.5\mbox{V to +5.5}\mbox{V} \end{array}$

Current Applied to Output

% in LOW State (Max) twice the rated I_{OL} (mA) ESD Last Passing Voltage (Min) 4000V

Recommended Operating Conditions

Free Air Ambient Temperature 0°C to +70°C Supply Voltage +4.5V to +5.5V

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

DC Electrical Characteristics

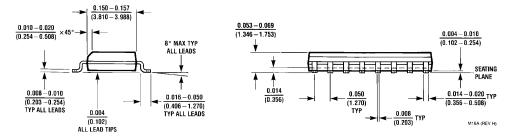
Symbol	mbol Parameter			Тур	Max	Units	V _{CC}	Conditions
V _{IH}	Input HIGH Voltage		2.0			V		Recognized as a HIGH Signal
V _{IL}	Input LOW Voltage				0.8	V		Recognized as a LOW Signal
V _{CD}	Input Clamp Diode Voltage				-1.2	V	Min	$I_{IN} = -18 \text{ mA}$
V _{OH}	Output HIGH Voltage	10% V _{CC} 5% V _{CC}	2.5 2.7			V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$
V _{OL}	Output LOW Voltage	10% V _{CC}			0.5	V	Min	I _{OL} = 20 mA
I _{IH}	Input HIGH Current				5.0	μА	Max	V _{IN} = 2.7V
I _{BVI}	Input HIGH Current Breakdown Test				7.0	μА	Max	V _{IN} = 7.0V
I _{CEX}	Output HIGH Leakage Current				50	μА	Max	V _{OUT} = V _{CC}
V _{ID}	Input Leakage Test		4.75			V	0.0	$I_{\text{ID}} = 1.9 \mu\text{A}$ All Other Pins Grounded
I _{OD}	Output Leakage Circuit Current				3.75	μА	0.0	V _{IOD} = 150 mV All Other Pins Grounded
I _{IL}	Input LOW Current				-0.6	mA	Max	$V_{IN} = 0.5V$
I _{OS}	Output Short-Circuit Current		-60		-150	mA	Max	V _{OUT} = 0V
I _{CCH}	Power Supply Current			13	20	mA	Max	V _O = HIGH
I _{CCL}	Power Supply Current			13	20	mA	Max	$V_O = LOW$

AC Electrical Characteristics

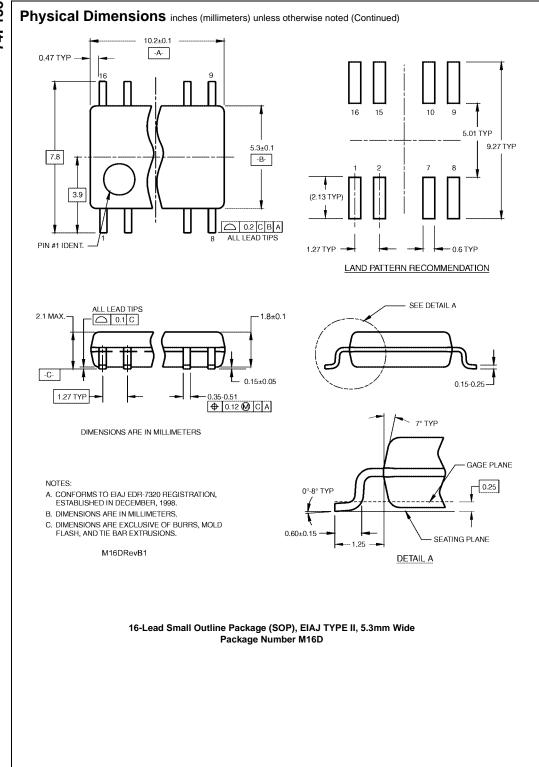
Symbol	Parameter		$T_A = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$		$T_A = 0$ °C $V_{CC} = C_L = 0$	Units			
		Min	Тур	Max	Min	Max			
t _{PLH}	Propagation Delay	3.5	5.6	7.5	3.5	8.5	ns		
t _{PHL}	A_n to \overline{O}_n	4.0	6.1	8.0	4.0	9.0	113		
t _{PLH}	Propagation Delay	3.5	5.4	7.0	3.5	8.0	20		
t _{PHL}	\overline{E}_1 or \overline{E}_2 to \overline{O}_n	3.0	5.3	7.0	3.0	7.5	ns		
t _{PLH}	Propagation Delay	4.0	6.2	8.0	4.0	9.0	ne		
t _{PHL}	E_3 to \overline{O}_n	3.5	5.6	7.5	3.5	8.5	ns		

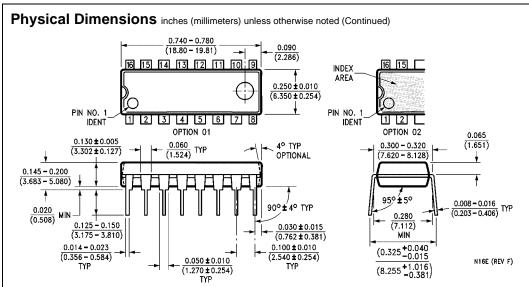
0.010 (0.254) MAX

LEAD NO.1



16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M16A





16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

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