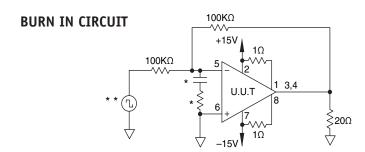


## Table 4 Group A Inspection

SG	PARAMETER	SYMBOL	TEMP.	POWER	TEST CONDITIONS	MIN	MAX	UNITS
1 1 1 1 1 1 1	Quiescent current Input offset voltage Input offset voltage Input offset voltage Input bias current, +IN Input bias current, -IN Input offset current	I	25°C 25°C 25°C 25°C 25°C 25°C 25°C	±15V ±15V ±7V ±19V ±15V ±15V ±15V	$\begin{aligned} &V_{IN} = 0,  A_{V} = 100,  R_{CL} = .2\Omega \\ &V_{IN} = 0,  A_{V} = 100 \\ &V_{IN} = 0,  A_{V} = 100 \\ &V_{IN} = 0,  A_{V} = 100 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \end{aligned}$		40 10 11.6 10.8 200 200 100	mA mV mV mV pA pA
3 3 3 3 3 3	Quiescent current Input offset voltage Input offset voltage Input offset voltage Input bias current, +IN Input bias current, -IN Input offset current		-55°C -55°C -55°C -55°C -55°C -55°C	±15V ±15V ±7V ±19V ±15V ±15V ±15V	$\begin{split} &V_{_{1N}}=0,A_{_{V}}=100,R_{_{CL}}=.2\Omega\\ &V_{_{1N}}=0,A_{_{V}}=100\\ &V_{_{1N}}=0,A_{_{V}}=100\\ &V_{_{1N}}=0,A_{_{V}}=100\\ &V_{_{1N}}=0\\ &V_{_{1N}}=0\\ &V_{_{1N}}=0\\ \end{split}$		60 14 15.6 14.8 200 200	mA mV mV pA pA pA
2 2 2 2 2 2 2	Quiescent current Input offset voltage Input offset voltage Input offset voltage Input bias current, +IN Input bias current, -IN Input offset current		125°C 125°C 125°C 125°C 125°C 125°C 125°C	±15V ±15V ±7V ±19V ±15V ±15V	$\begin{split} &V_{_{1N}}=0,A_{_{V}}=100,R_{_{CL}}=.2\Omega\\ &V_{_{1N}}=0,A_{_{V}}=100\\ &V_{_{1N}}=0,A_{_{V}}=100\\ &V_{_{1N}}=0,A_{_{V}}=100\\ &V_{_{1N}}=0\\ &V_{_{1N}}=0\\ &V_{_{1N}}=0\\ \end{split}$		60 15 16.6 15.8 30 30	mA mV mV mV nA nA
4 4 4 4 4 4	Output voltage, $I_{\rm O} = 5{\rm A}$ Output voltage, $I_{\rm O} = 36{\rm mA}$ Output voltage, $I_{\rm O} = 2{\rm A}$ Current limits Stability/noise Slew rate Open loop gain Common mode rejection	V <sub>o</sub> V <sub>o</sub> I <sub>CL</sub> E <sub>N</sub> SR A <sub>OL</sub> CMR	25°C 25°C 25°C 25°C 25°C 25°C 25°C 25°C	±9V ±19V ±12V ±9V ±15V ±15V ±15V ±8.25V	$\begin{split} R_L &= 1\Omega, \ R_{CL} = 0\Omega \\ R_L &= 500\Omega \\ R_L &= 5\Omega, \ R_{CL} = 0\Omega \\ R_L &= 5\Omega, \ R_{CL} = 1\Omega \\ R_L &= 500\Omega, \ A_V = 1, \ C_L = 1.5nF \\ R_L &= 500\Omega \\ R_L &= 500\Omega, \ F = 10Hz \\ R_L &= 500\Omega, \ F = DC, \ V_{CM} = \pm 2.25V \end{split}$	5 18 10 .54 13 86 70	.86 1 100	V V V A mV V/µs dB dB
6 6 6 6 6 6	Output voltage, $I_0 = 5A$ Output voltage, $I_0 = 36mA$ Output voltage, $I_0 = 2A$ Stability/noise Slew rate Open loop gain Common mode rejection	V <sub>o</sub> V <sub>o</sub> E <sub>N</sub> SR A <sub>oL</sub> CMR	-55°C -55°C -55°C -55°C -55°C -55°C	±9V ±19V ±12V ±15V ±18V ±15V ±8.25V	$\begin{split} R_L &= 1\Omega,  R_{CL} = 0\Omega \\ R_L &= 500\Omega \\ R_L &= 5\Omega,  R_{CL} = 0\Omega \\ R_L &= 500\Omega,  A_V = 1,  C_L = 1.5 n F \\ R_L &= 500\Omega \\ R_L &= 500\Omega,  F = 10 Hz \\ R_L &= 500\Omega,  F = DC,  V_{CM} = \pm 2.25 V \end{split}$	5 18 10 13 86 70	1 100	V V V mV V/µs dB dB
5 5 5 5 5 5 5	Output voltage, $I_0 = 3A$ Output voltage, $I_0 = 36mA$ Output voltage, $I_0 = 2A$ Stability/noise Slew rate Open loop gain Common mode rejection	V <sub>o</sub> V <sub>o</sub> E <sub>N</sub> SR A <sub>oL</sub> CMR	125°C 125°C 125°C 125°C 125°C 125°C 125°C	±7V ±19V ±12V ±15V ±18V ±15V ±8.25V	$\begin{split} R_{L} &= 1\Omega,  R_{CL} = 0\Omega \\ R_{L} &= 500\Omega \\ R_{L} &= 5\Omega,  R_{CL} = 0\Omega \\ R_{L} &= 500\Omega,  A_{V} = 1,  C_{L} = 1.5 n F \\ R_{L} &= 500\Omega,  F = 10 Hz \\ R_{L} &= 500\Omega,  F = DC,  V_{CM} = \pm 2.25 V \end{split}$	3 18 10 8.5 86 70	1 100	V V V mV V/µs dB dB



- \* These components are used to stabilize device due to poor high frequency characteristics of burn in board.
- \*\* Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.



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