

High Side Switch ICs 2ch

BD2046AFJ BD2056AFJ

General Description

High side switch for USB is a switch with over current protection used in power supply line of universal serial bus (USB). Its switch unit has two channels of N-channel power MOSFET. Over current detection circuit, thermal shutdown circuit, under voltage lockout and soft start circuit are built in.

Features

- Dual N-MOS high side switch
- Continuous current load 0.25A
- Control input logic

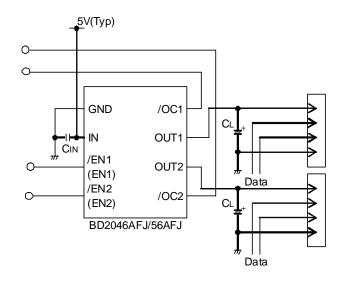
Active-Low: BD2046AFJActive-High: BD2056AFJ

- Soft start circuit
- Over current detection
- Thermal shutdown
- Under voltage lockout
- Open drain error flag output
- Reverse-current protection when switch off
- Flag output delay filter built in

Applications

USB hub in consumer appliances, Car accessory, PC, PC peripheral equipment, and so forth

Typical Application Circuit



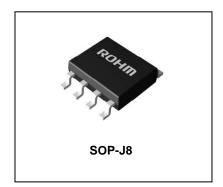
Lineup

Ove	er current thres	hold	Control input logic	D	nakaga	Orderable Part Number	
Min.	Тур.	Max.	Control input logic	Package		Orderable Part Number	
0.3A	0.5A	0.9A	Low	SOP-J8	Reel of 2500	BD2046AFJ – E2	
0.3A	0.5A	0.9A	High	SOP-J8	Reel of 2500	BD2056AFJ – E2	

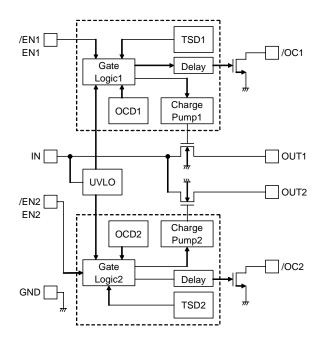
Key Specifications

Input voltage range: 2.7V to 5.5V
 ON resistance: 100mΩ(Typ.)
 Over current threshold: 0.3A min., 0.9A max.
 Standby current: 0.01μA (Typ.)
 Operating temperature range: -40°C to +85°C

●Package SOP-J8 W(Typ.) D(Typ.) H (Max.) 4.90mm x 6.00mm x 1.65mm



●Block Diagram



Pin Configurations BD2046AFJ BD2056AFJ **TOP VIEW TOP VIEW GND** /OC1 **GND** 1 8 /OC1 1 8 OUT1 2 IN 7 2 IN OUT1 7 /EN1 3 OUT2 6 3 EN1 OUT2 6 4 /EN2 /OC2 5 /OC2 5 4 EN2

●Pin Descriptions

OBD2046AFJ

 D20+0/ (I 0			
Pin No.	Symbol	1/0	Pin function
1	GND	I	Ground.
2	IN	I	Power supply input. Input terminal to the switch and power supply input terminal of the internal circuit.
3, 4	/EN	I	Enable input. Switch on at Low level. High level input > 2.0V, Low level input < 0.8V.
5, 8	/OC	0	Error flag output. Low at over current, thermal shutdown. Open drain output.
6, 7	OUT	0	Switch output.

OBD2056AFJ

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5, 8	/OC	0	Error flag output. Low at over current, thermal shutdown. Open drain output.
6, 7	OUT	0	Switch output.

● Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Supply voltage	Vin	-0.3 to 6.0	V
Enable voltage	VEN, V/EN	-0.3 to 6.0	V
/OC voltage	V/oc	-0.3 to 6.0	V
/OC current	IS/oc	10	mA
OUT voltage	Vout	-0.3 to 6.0	V
Storage temperature	Tstg	-55 to 150	ô
Power dissipation	Pd	560 ^{*1}	mW

^{*1} In the case of exceeding Ta = 25°C, 4.48mW should be reduced per 1°C.

Recommended Operating Range

Parameter	Symbol	Ratings	Unit				
Operating voltage	Vin	2.7 to 5.5	V				
Operating temperature	Topr	-40 to 85	°C				
Continuous output current	ILO	0 to 250	mA				

• Electrical Characteristics

OBD2046AFJ (Unless otherwise specified, VIN = 5.0V, Ta = 25°C)

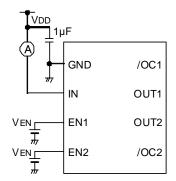
Parameter	Symbol	Limits		Unit	Condition	
Farameter	Syllibol	Min.	Тур.	Max.	Offic	Condition
Operating Current	IDD	-	110	140	μΑ	V/EN = 0V, OUT = OPEN
Standby Current	ISTB	-	0.01	1	μA	V/EN = 5V, OUT = OPEN
		2.0	-	-	V	High input
/EN input voltage	V/EN	-	-	0.8	V	Low input
		-	-	0.4	V	Low input 2.7V≤ VIN ≤4.5V
/EN input current	I/EN	-1.0	0.01	1.0	μΑ	V/EN = 0V or V/EN = 5V
/OC output LOW voltage	V/oc	-	-	0.5	V	I/oc = 5mA
/OC output leak current	IL/oc	-	0.01	1	μA	V/oc = 5V
ON resistance	Ron	-	100	130	mΩ	IOUT = 250mA
Over-current Threshold	Ітн	0.3	0.5	0.9	А	
Output current at short	Isc	0.3	0.5	0.7	Α	VIN = 5V, VOUT = 0V, CL = 100µF (RMS)
Output rise time	Ton1	-	1.8	10	ms	
Output turn on time	Ton2	-	2.1	20	ms	D. 200 C. OPEN
Output fall time	Toff1	-	1	20	μs	$RL = 20\Omega$, $CL = OPEN$
Output turn off time	Toff2	-	3	40	μs	
10/10 45	VTUVH	2.1	2.3	2.5	V	Increasing VIN
UVLO threshold	VTUVL	2.0	2.2	2.4	V	Decreasing VIN

● Electrical Characteristics - continued

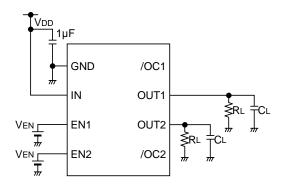
OBD2056AFJ (Unless otherwise specified, VIN = 5.0V, Ta = 25°C)

DBD2056AFJ (Unless otherwise specified, VIN = 5.0V, Ta = 25°C) Limits					<u> </u>	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Operating Current	IDD	-	110	140	μΑ	VEN = 5V , OUT = OPEN
Standby Current	ISTB	-	0.01	1	μΑ	VEN = 0V , OUT = OPEN
		2.0	-	-	V	High input
/EN input voltage	VEN	-	-	0.8	V	Low input
		-	-	0.4	V	Low input 2.7V≤ VIN ≤4.5V
/EN input current	len	-1.0	0.01	1.0	μΑ	VEN = 0V or VEN = 5V
/OC output LOW voltage	V/oc	-	-	0.5	V	I/oc = 5mA
/OC output leak current	IL/oc	-	0.01	1	μΑ	V/oc = 5V
ON resistance	Ron	-	100	130	mΩ	IOUT = 250mA
Over-current Threshold	Ітн	0.3	0.5	0.9	Α	
Output current at short	Isc	0.3	0.5	0.7	Α	VIN = 5V , VOUT = 0V, CL = 100μF (RMS)
Output rise time	Ton1	-	1.8	10	ms	
Output turn on time	Ton2	-	2.1	20	ms	D. 200 C. ODEN
Output fall time	Toff1	-	1	20	μs	$RL = 20\Omega$, $CL = OPEN$
Output turn off time	Toff2	-	3	40	μs	
UVLO threshold	VTUVH	2.1	2.3	2.5	V	Increasing VIN
OVEO threshold	VTUVL	2.0	2.2	2.4	V	Decreasing VIN

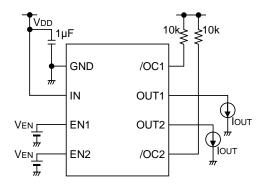
Measurement Circuit



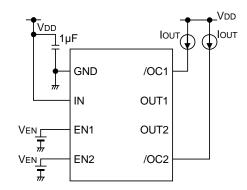
A. Operating Current



B. EN, /EN input Voltage, Output Rise, Fall Time



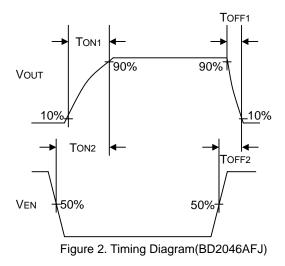
C.ON resistance, Over Current Cetection

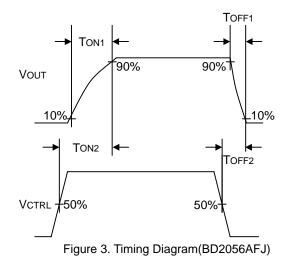


D. OC output LOW Voltage

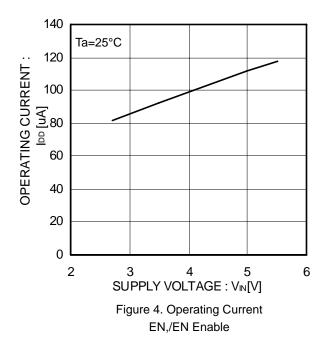
Figure 1. Measurement circuit

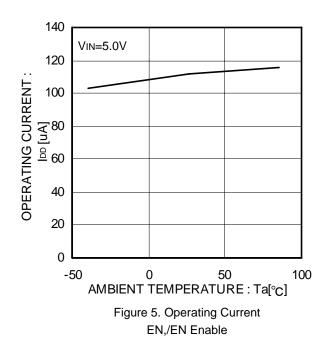
Timing Diagram

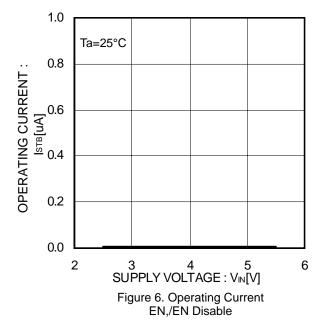


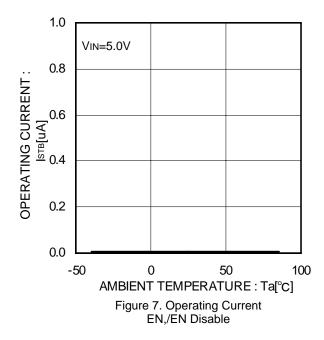


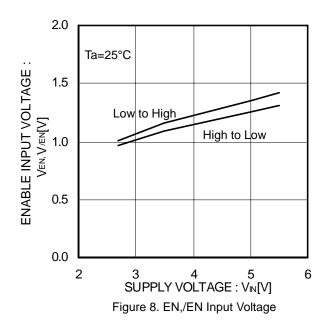
●Typical Performance Curves

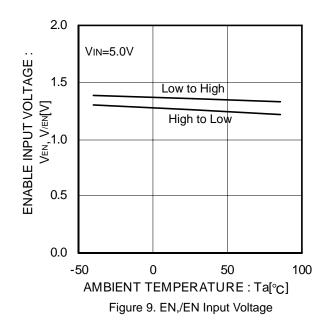


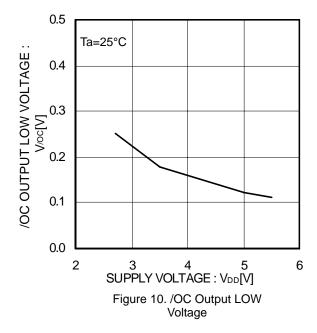


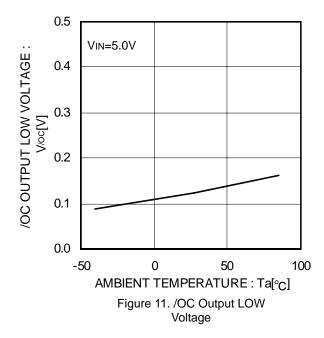


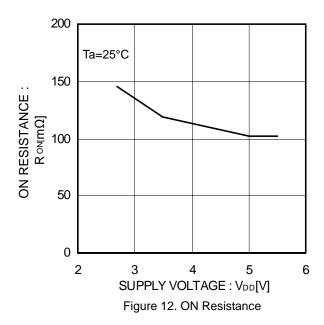


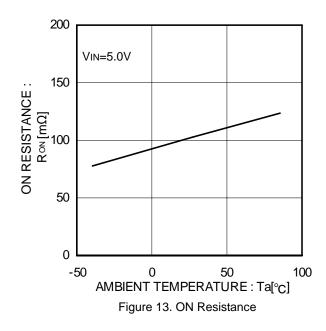


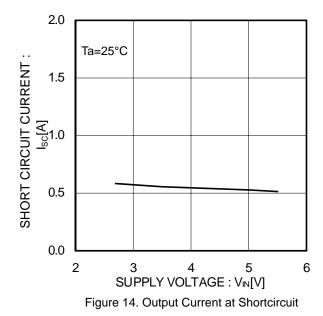


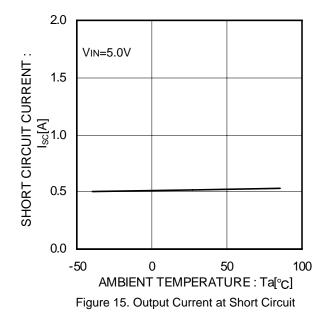


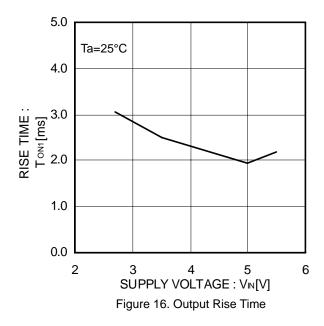


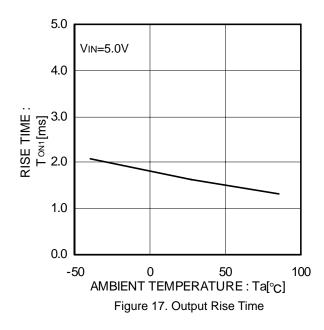


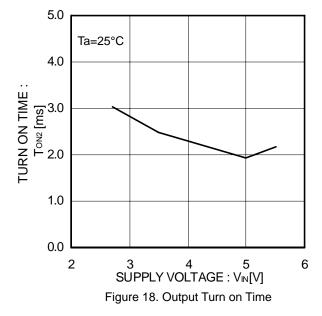


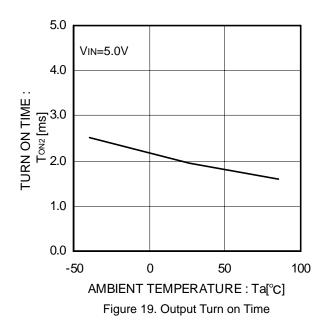


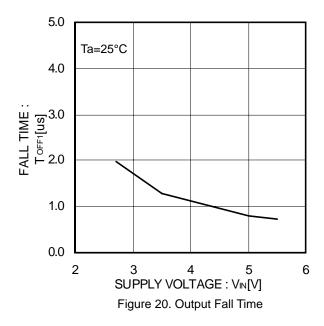


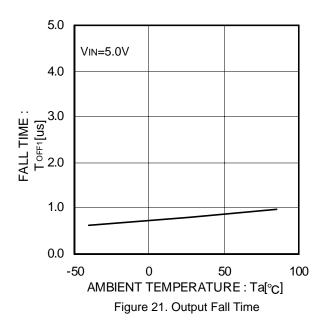


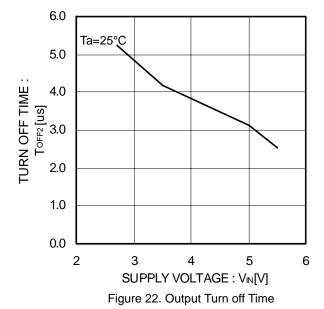


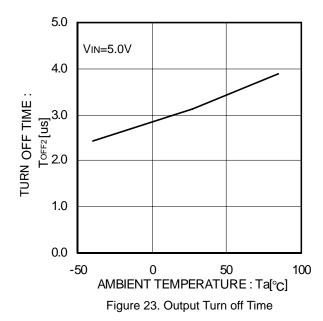


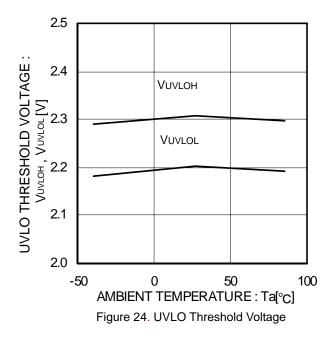


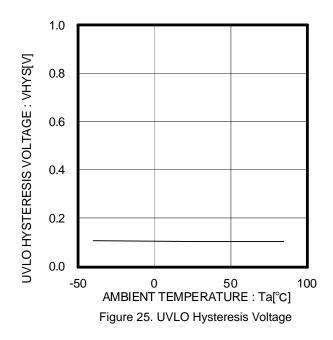




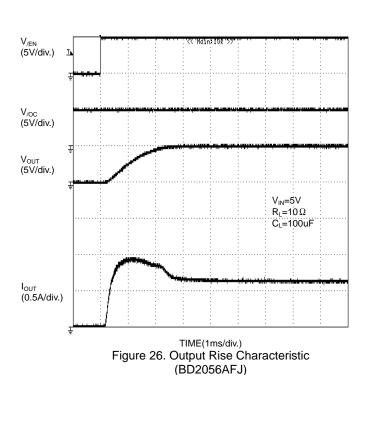


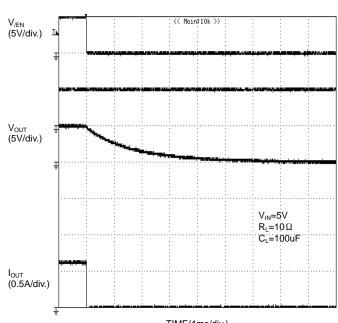




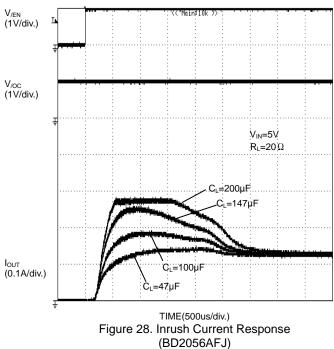


■Typical Wave Forms





TIME(1ms/div.)
Figure 27. Output Fall Characteristic (BD2056AFJ)



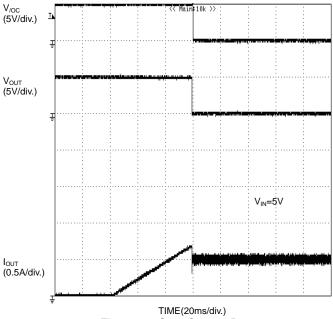
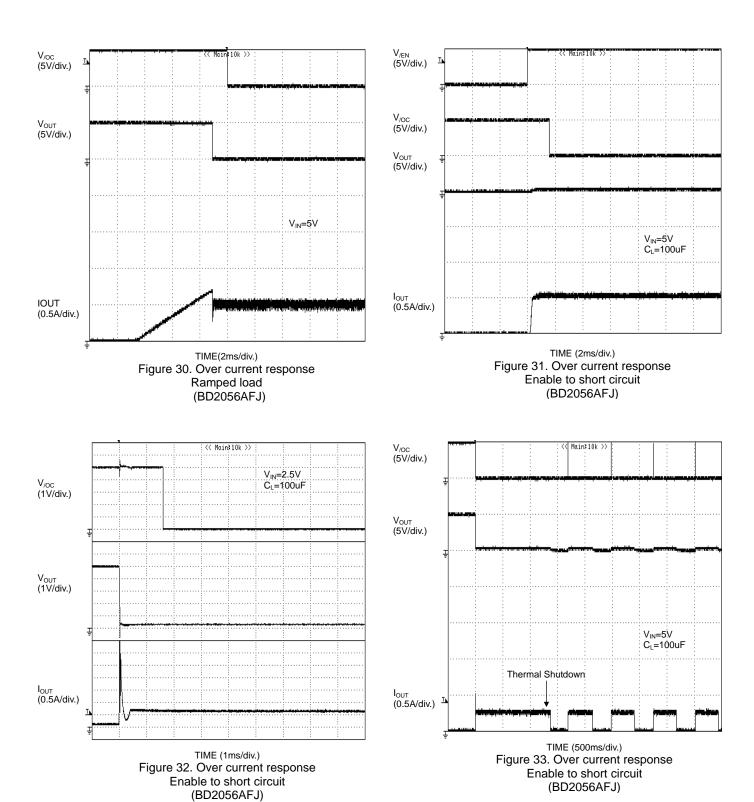
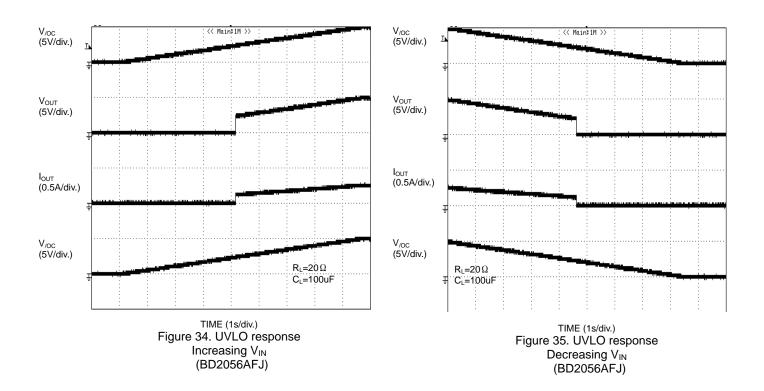


Figure 29. Over Current Response Ramped load (BD2056AFJ)

●Typical Wave Forms - continued

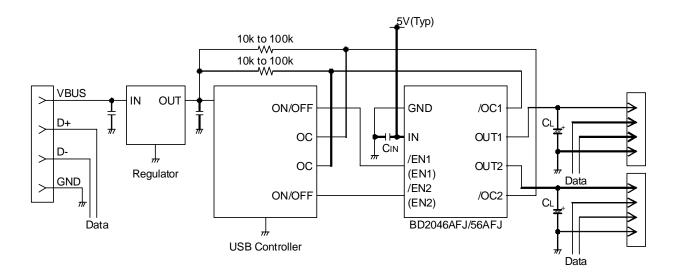


●Typical Wave Forms - continued



Regarding the output rise/fall and over current detection characteristics of BD2046AFJ, refer to the characteristic of BD2056AFJ.

● Typical Application Circuit



Application Information

When excessive current flows due to output short-circuit or so, ringing occurs because of inductance between power source line to IC, and may cause bad influences on IC operations. In order to avoid this case, connect a bypass capacitor across IN terminal and GND terminal of IC. 1µF or higher is recommended.

Pull up /OC output by resistance of $10k\Omega$ to $100k\Omega$.

Set up value which satisfies the application as CL.

This application circuit does not guarantee its operation.

When using the circuit with changes to the external circuit constants, make sure to leave an adequate margin for external components including AC/DC characteristics as well as dispersion of the IC.

● Functional Description

1. Switch operation

IN terminal and OUT terminal are connected to the drain and the source of switch MOSFET respectively. The IN terminal is used also as power source input to internal control circuit.

When the switch is turned on from EN/EN control input, the IN terminal and OUT terminal are connected by a $100m\Omega$ switch. In ON status, the switch is bidirectional. Therefore, when the potential of OUT terminal is higher than that of the IN terminal, current flows from OUT terminal to IN terminal.

Since a parasitic diode between the drain and the source of switch MOSFET is canceled, in the off status, it is possible to prevent current from flowing reversely from OUT to IN.

2. Thermal shutdown circuit (TSD)

Thermal shutdown circuit have dual thermal shutdown threshold. Since thermal shutdown works at a lower junction temperature when an overcurrent occurs, only the switch of an overcurrent state become off and error flag is output. Thermal shut down action has hysteresis. Therefore, when the junction temperature goes down, switch on and error flag output automatically recover. However, until cause of junction temperature increase such as output short circuit is removed or the switch is turned off, thermal shutdown detection and recovery are repeated. The thermal shutdown circuit works when the switch of either OUT1 or OUT2 is on (EN,/EN signal is active).

3. Over current detection (OCD)

The over current detection circuit limits current (I_{SC}) and outputs an error flag (/OC) when current flowing in each switch MOSFET exceeds a specified value. There are three types of response against over current. The over current detection circuit works when the switch is on (EN,/EN signal is active).

3-1. When the switch is turned on while the output is in short circuit status:

When the switch is turned on while the output is in short-circuit status, the switch goes into current limit status immediately.

3-2. When the output short circuits while the switch is on:

When the output short circuits or high-current load is connected while the switch is on, very large current flows until the over current limit circuit reacts. When the current detection limit circuit works, current limitation is carried out.

3-3. When the output current increases gradually:

When the output current increases gradually, current limitation does not work until the output current exceeds the over current detection value. When it exceeds the detection value, current limitation is carried out.

4. Under voltage lockout (UVLO)

UVLO circuit prevents the switch from turning on until the VIN exceeds 2.3V(Typ.). If the VIN drops below 2.2V(Typ) while the switch turns on, then UVLO shuts off the switch. UVLO has hysteresis of 100mV(Typ). Under voltage lockout circuit works when the switch of either OUT1 or OUT2 is on (EN,/EN signal is active).

5. Error flag (/OC) output

Error flag output is N-MOS open drain output. At At detection of over current and/or thermal shutdown, the output level is low

Over current detection has delay filter. This delay filter prevents instantaneous current detection such as rush current at switch on, hot plug from being informed to outside.

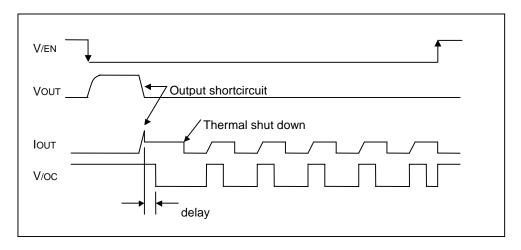


Figure 36. Over current detection, thermal shutdown timing (BD2046AFJ)

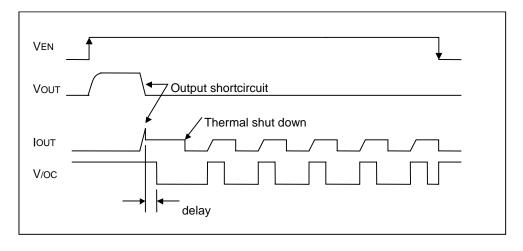


Figure 37. Over current detection, thermal shutdown timing (BD2056AFJ)

●Power Dissipation

(SOP-J8)

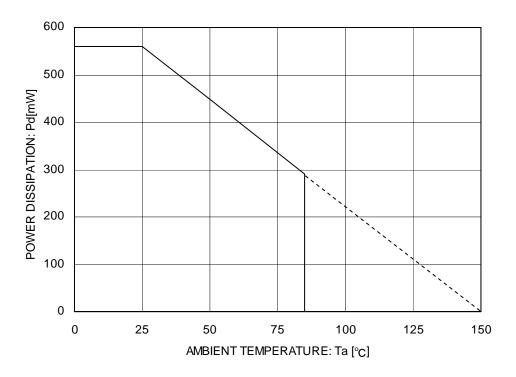


Figure 38. Power dissipation curve (Pd-Ta Curve)

●I/O Equivalence Circuit

Symbol	Pin No	Equivalence circuit
EN1(/EN1) EN2(/EN2)	3, 4	/EN1(EN1) /EN2(EN2)
/OC1 /OC2	5, 8	/OC1 /OC2
OUT1 OUT2	6, 7	OUT1 OUT2

Operational Notes

(1) Absolute maximum ratings

Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings.

(2) Recommended operating conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

(3) Reverse connection of power supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply terminals.

(4) Power supply lines

Design the PCB layout pattern to provide low impedance ground and supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

(5) Ground Voltage

The voltage of the ground pin must be the lowest voltage of all pins of the IC at all operating conditions. Ensure that no pins are at a voltage below the ground pin at any time, even during transient condition.

(6) Short between pins and mounting errors

Be careful when mounting the IC on printed circuit boards. The IC may be damaged if it is mounted in a wrong orientation or if pins are shorted together. Short circuit may be caused by conductive particles caught between the pins.

(7) Operation under strong electromagnetic field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

(8) Testing on application boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

(9) Regarding input pins of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode

When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.

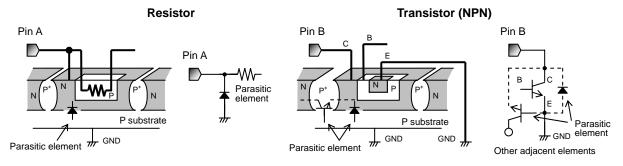


Figure 39. Example of monolithic IC structure

(10) GND wiring pattern

When using both small-signal and large-current GND traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the GND traces of external components do not cause variations on the GND voltage. The power supply and ground lines must be as short and thick as possible to reduce line impedance.

(11) External Capacitor

When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

(12) Thermal shutdown circuit (TSD)

The IC incorporates a built-in thermal shutdown circuit, which is designed to turn off the IC when the internal temperature of the IC reaches a specified value. Do not continue to operate the IC after this function is activated. Do not use the IC in conditions where this function will always be activated.

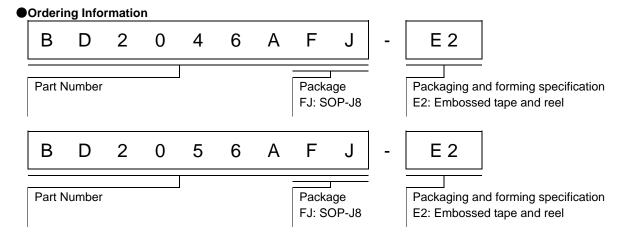
(13) Thermal consideration

Use a thermal design that allows for a sufficient margin by taking into account the permissible power dissipation (Pd) in actual operating conditions. Consider Pc that does not exceed Pd in actual operating conditions (Pc≥Pd).

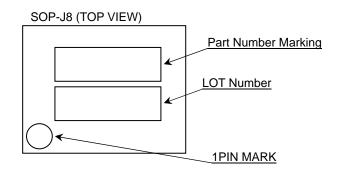
Package Power dissipation : Pd (W)=(Tjmax-Ta)/ θ ja Power dissipation : Pc (W)=(Vcc-Vo)×Io+Vcc×Ib

Tjmax: Maximum junction temperature=150°C, Ta: Peripheral temperature[°C],

 θ ja : Thermal resistance of package-ambience[°C/W], Pd : Package Power dissipation [W], Pc : Power dissipation [W], Vcc : Input Voltage, Vo : Output Voltage, Io : Load, Ib : Bias Current

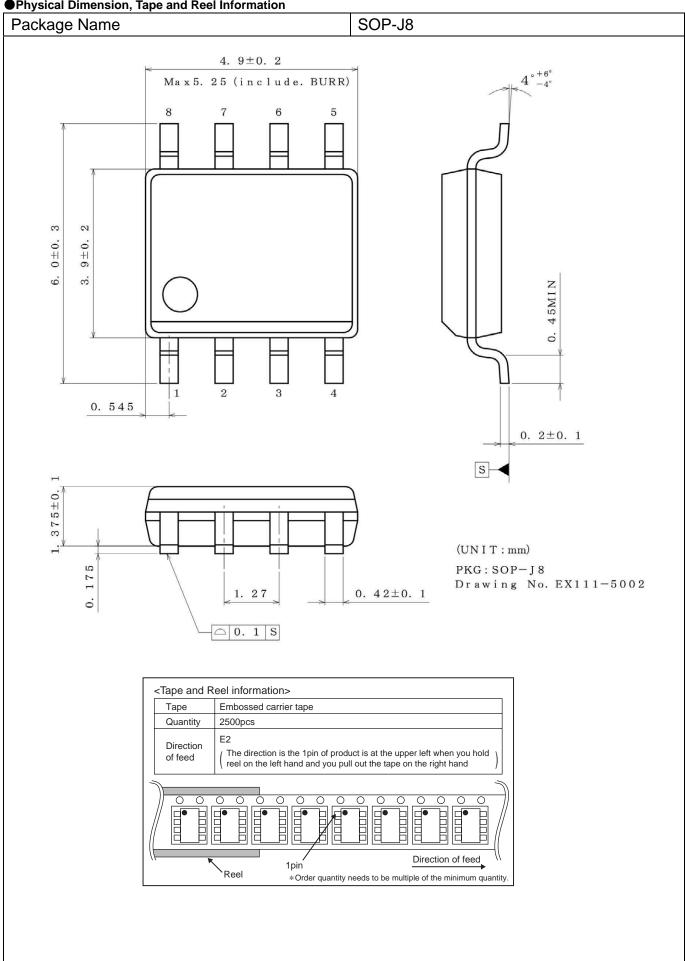


Marking Diagram



Part Number	Part Number Marking
BD2046AFJ	D046A
BD2056AFJ	D056A

●Physical Dimension, Tape and Reel Information



Revision History

Date	Revision	Changes
11.Mar.2013	001	New Release

Notice

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Precaution on using ROHM Products

- Our Products are designed and manufactured for application in ordinary electronic equipments (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment, transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.
- 2) ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
 - [a] Installation of protection circuits or other protective devices to improve system safety
 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3) Our Products are designed and manufactured for use under standard conditions and not under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4) The Products are not subject to radiation-proof design.
- 5) Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6) In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse) is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7) De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8) Confirm that operation temperature is within the specified range described in the product specification.
- 9) ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1) When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2) In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

●Precautions Regarding Application Examples and External Circuits

- If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

●Precaution for Storage / Transportation

- 1) Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2) Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3) Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4) Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

●Precaution for Product Label

QR code printed on ROHM Products label is for ROHM's internal use only.

Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

● Precaution for Foreign Exchange and Foreign Trade act

Since our Products might fall under controlled goods prescribed by the applicable foreign exchange and foreign trade act, please consult with ROHM representative in case of export.

Precaution Regarding Intellectual Property Rights

- All information and data including but not limited to application example contained in this document is for reference only. ROHM does not warrant that foregoing information or data will not infringe any intellectual property rights or any other rights of any third party regarding such information or data. ROHM shall not be in any way responsible or liable for infringement of any intellectual property rights or other damages arising from use of such information or data.:
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Other Precaution

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