

HCS361 Errata Sheet

The HCS361 parts you have received conform functionally to the Device Data Sheet (**DS40146D**), except for the anomalies described below.

None.

Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (**DS40146D**), the following clarifications and corrections should be noted.

Page 16 of **DS40146D**, entitled "KEELOQ® Code Hopping Encoder", for Microchip part HCS361 has been changed. Table 6-1, "Programming/Verify Timing Requirements" contains updated information in the Program cycle time (TWC), Clock low time (TCLKL) and Clock high time (TCLKH) parameters. The table below has been revised to reflect these changes.

TABLE 1: PROGRAMMING/VERIFY TIMING REQUIREMENTS

VDD = 5.0V ± 10% 25 °C ± 5 °C				
Parameter	Symbol	Min.	Max.	Units
Program mode setup time	T2	0	5.0	ms
Hold time 1	T1	9.0	—	ms
Program cycle time	TWC	—	50	ms
Clock low time	TCLKL	50	—	µs
Clock high time	TCLKH	50	—	µs
Data setup time	TDS	0	—	µs
Data hold time	TDH	18	—	µs
Data out valid time	TDV	—	24	µs

HCS361

REVISION HISTORY

Rev A Document

First revision of this document.

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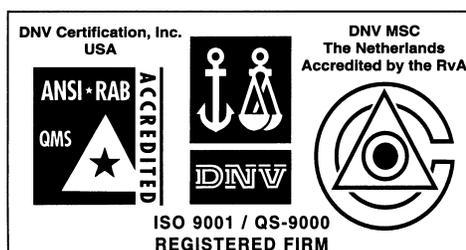
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