5LP01SP

P-Channel Small Signal MOSFET -50V, -0.07A, 23Ω, Single SPA



http://onsemi.com

Features

- · Low ON-resistance
- · Ultrahigh-speed switching
- 2.5V drive
- · Protection diode in

Specifications

Absolute Maximum Ratings at Ta=25°C

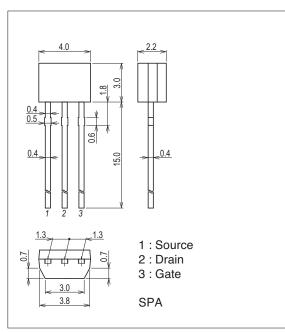
Parameter	Symbol	Conditions	value	Unit
Drain to Source Voltage	V _{DSS}		-50	V
Gate to Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		-0.07	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-0.28	Α
Power Dissipation	PD		0.25	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

This product is designed to "ESD immunity < 200V*", so please take care when handling.

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Package Dimensions

unit : mm (typ) 7524-007



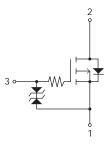
Ordering & Package Information

Device	Package	Shipping	memo	
5LP01SP	SPA SC-72	500pcs./bag	Db Fran	
5LP01SP-AC	SPA SC-72	2,500pcs./box	- Pb-Free	

Marking



Electrical Connection



^{*} Machine Model

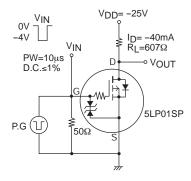
5LP01SP

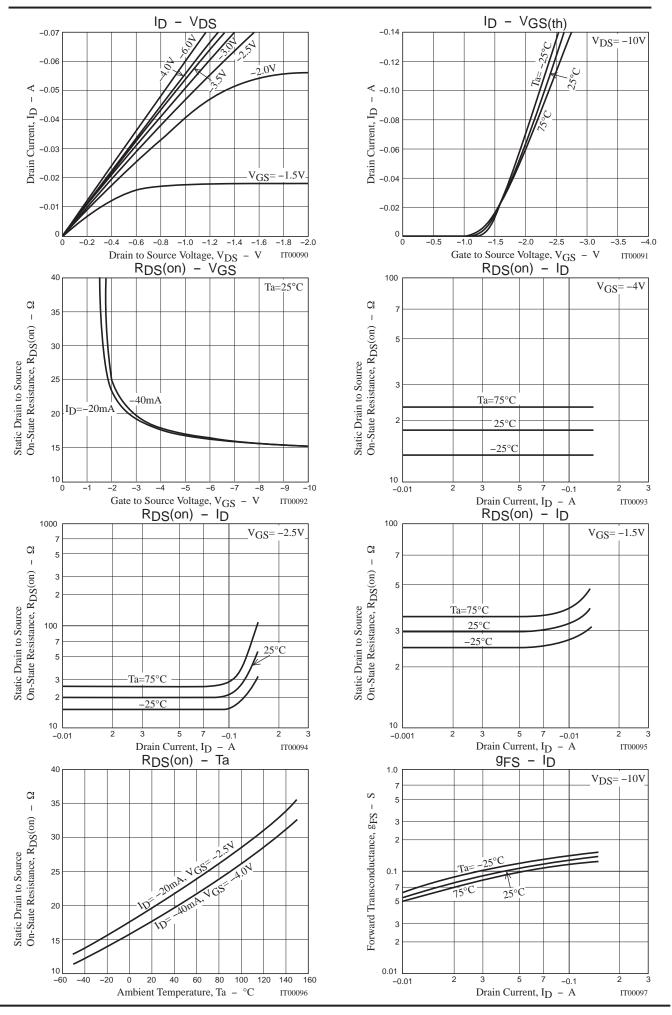
Electrical Characteristics at Ta=25°C

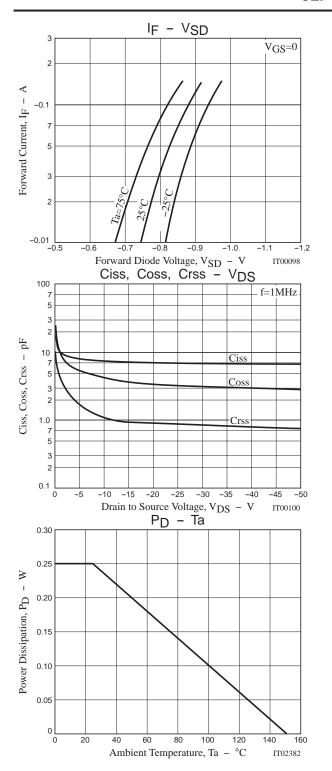
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-50			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-50V, V _{GS} =0V			10	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ
Gate Threshold Voltage	V _{GS} (th)	V _{DS} =-10V, I _D =-100μA	-0.4		-1.4	V
Forward Transconductance	9FS	V _D S=-10V, I _D =-40mA	70	100		mS
Static Drain to Source On-State Resistance	R _{DS} (on)1	I _D =-40mA, V _G S=-4V		18	23	Ω
	R _{DS} (on)2	I _D =-20mA, V _G S=-2.5V		20	28	Ω
	R _{DS} (on)3	I _D =-5mA, V _{GS} =-1.5V		30	60	Ω
Input Capacitance	Ciss	V _{DS} =-10V, f=1MHz		7.4		pF
Output Capacitance	Coss			4.2		pF
Reverse Transfer Capacitance	Crss			1.3		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		20		ns
Rise Time	t _r			35		ns
Turn-OFF Delay Time	t _d (off)			160		ns
Fall Time	t _f			150		ns
Total Gate Charge	Qg	V _{DS} =-10V, V _{GS} =-10V, I _D =-70mA		1.40		nC
Gate to Source Charge	Qgs			0.16		nC
Gate to Drain "Miller" Charge	Qgd]		0.23		nC
Forward Diode Voltage	V _{SD}	I _S =-70mA, V _G S=0V		0.85	1.2	V

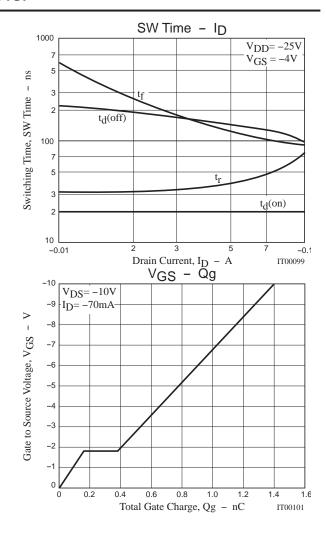
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit



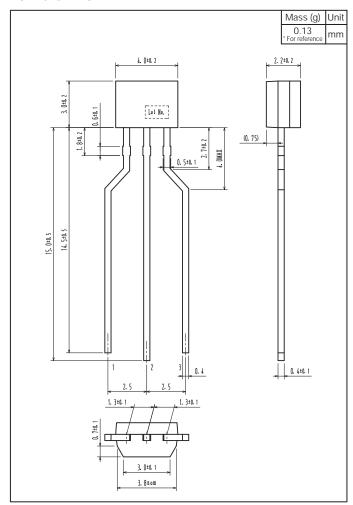






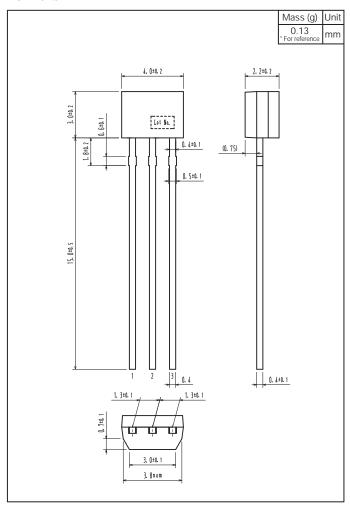
Outline Drawing

5LP01SP-AC



Outline Drawing

5LP01SP



Note on usage: Since the 5LP01SP is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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