

## STEVAL-MKI080V1

## MEMS demonstration board based on the LPR410AL analog output 2-axis gyroscope

Data brief

#### **Features**

- Two different working modes:
  - analog (AWM)
  - digital (DWM)
- RoHS compliant

### Description

The STEVAL-MKI080V1 demonstration board is designed to provide the user with a complete, ready-to-use platform for demonstration of the LPR410AL product family.

The STEVAL-MKI080V1 includes a sensing element and an IC interface capable of translating information from the sensing element into a measured signal that can be used for external applications.

In addition to the MEMS sensor, the demonstration board uses an ST7 microcontroller which functions as a bridge between the sensor and the PC. This makes it possible to download the graphical user interface (GUI) from the website or to use dedicated software routines for customized applications.

The STEVAL-MKI080V1 demonstration board has been designed for use in two different working modes: analog and digital.

In analog mode (AWM) the microcontroller is disabled and the analog outputs of the device are available to the user on a dedicated connector. This is the default working mode when power is supplied through either the USB connector or the supply connector.

In digital mode (DWM) the microcontroller is enabled and allows the user to digitally acquire the output signals of the device, to view them on the PC through the dedicated GUI, and to manage the control pins of the device.

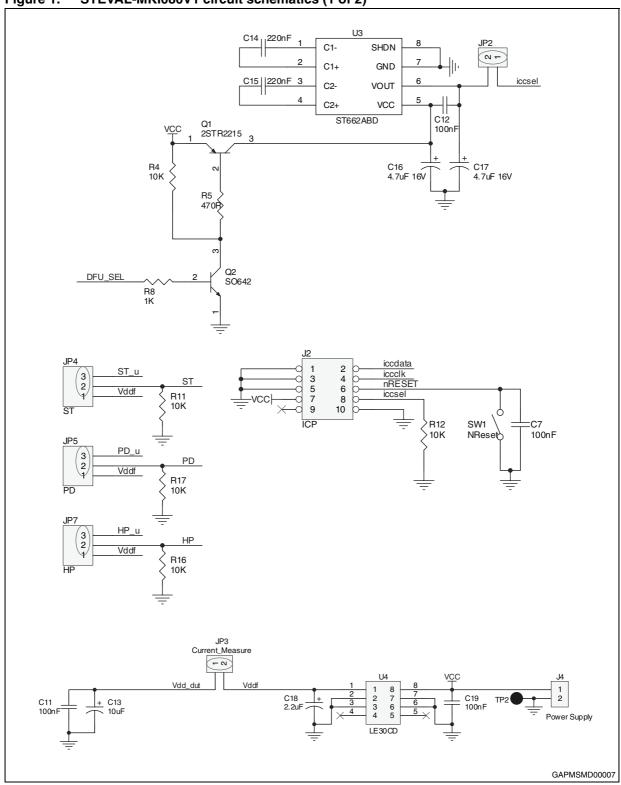


STEVAL-MKI080V1

Schematic diagrams STEVAL-MKI080V1

## 1 Schematic diagrams

Figure 1. STEVAL-MKI080V1 circuit schematics (1 of 2)



R1 180R oscout Vddf oscir V+ 2 DM 3 DP 3 nc GND 5 USB\_mini\_B C1 10u<u>F</u> C4 C2 100nF 4.7<u>u</u>F C5 100nf R2 R3 100R 100R U2 ST72F651AR6T1E Gled Rled - UVss - UDM - UDP - UVcc - UVdd - Vddf - Vssf - PE5 - PE6 - PE7 - PB0 - PB1 - PB2 - PB3 - PB4 - PB5 PWM0 AIN6 AIN5 AIN4 AIN3 AIN2 OCMP2 OCMP1 R6 1.5K Vddf Rled Gled DFU\_SE OutAna2 C9 220nF C8 47nF Vdď R7 10K SW2 ႌ ST FILTVDD VCONT U1 88 27 R19 0R R13 33K R18 0R **2** 34 19 Vdd\_dut NC Vdd C24 33nF SR14 DNM JP8 R15 DNM 18 FILTVDD C25 33nF NC FILTVDD 17 VCONT VCONT 16 4xIN\_X 4xIN Y × 6 NC Out1 -C26 C27 tbd

Figure 2. STEVAL-MKI080V1 circuit schematics (2 of 2)

GAPMSMD00008

Revision history STEVAL-MKI080V1

# 2 Revision history

Table 1. Document revision history

Date	Revision	Changes
04-Mar-2011	1	Initial release.

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