

4V Drive Nch MOSFET

RSD050N06

●Structure

Silicon N-channel MOSFET

Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 3) Parallel use is easy.

Applications

Switching

Packaging specifications

Type	Package	CPT3	
	Code	TL	
	Basic ordering unit (pieces)	2500	

● Absolute maximum ratings (T_a=25°C)

Parar	Symbol	Limits	Unit	
Drain-source voltage		V_{DSS}	60	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	Continuous	I _D	±5.0	Α
	Pulsed	I _{DP} *1	±15	Α
Source current	Continuous	I _S	5.0	Α
(Body Diode)	Pulsed	I _{SP} *1	15	Α
Power dissipation		P _D *2	15	W
Channel temperature		T _{ch}	150	°C
Range of storage temperature		T _{stg} –55 to +150		°C

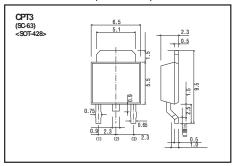
^{*1} Pw≤10μs, Duty cycle≤1%

●Thermal resistance

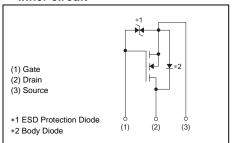
Parameter	Symbol	Limits	Unit
Channel to Case	R _{th (ch-c)} *	8.33	°C/W

^{*} T_c=25°C

• Dimensions (Unit : mm)



• Inner circuit



^{*2} T_C=25°C

●Electrical characteristics (T_a=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}		-	±10	μA	V_{GS} =±20V, V_{DS} =0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	60	-	-	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	1	-	1	μA	V _{DS} =60V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	1.0	1	3.0	٧	V_{DS} =10V, I_{D} =1mA
Static ducin accuracy on state	*	1	78	109		I _D =5.0A, V _{GS} =10V
Static drain-source on-state resistance	R _{DS (on)}	1	94	131	mΩ	I _D =5.0A, V _{GS} =4.5V
		1	100	140		I _D =5.0A, V _{GS} =4.0V
Forward transfer admittance	I Y _{fs} I [*]	3.5	-	-	S	I _D =5.0A, V _{DS} =10V
Input capacitance	C _{iss}	-	290	-	pF	V _{DS} =10V
Output capacitance	C _{oss}	1	90	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	1	35	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	-	8	-	ns	I _D =2.5A, V _{DD} ≒ 30V
Rise time	t _r *	1	17	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)} *	1	26	-	ns	$R_L=12\Omega$
Fall time	t _f *	-	8	-	ns	$R_G=10\Omega$
Total gate charge	Q _g *	-	8.0	-	nC	V _{DD} ≒ 30V
Gate-source charge	Q _{gs} *	-	1.4	-	nC	I _D =5.0A
Gate-drain charge	Q _{gd} *	-	1.4	-	nC	V _{GS} =10V

^{*}Pulsed

●Body diode characteristics (Source-Drain) (T_a=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.2	V	I _s =5.0A, V _{GS} =0V

^{*}Pulsed

●Electrical characteristic curves (Ta=25°C)

Fig.1 Typical Output Characteristics (I)

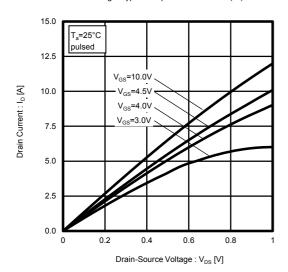


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

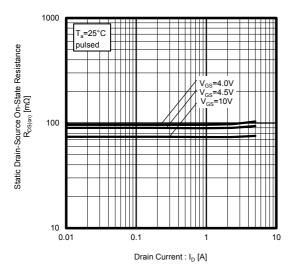


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

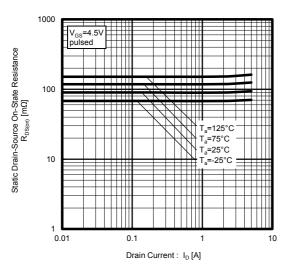


Fig.2 Typical Output Characteristics (II)

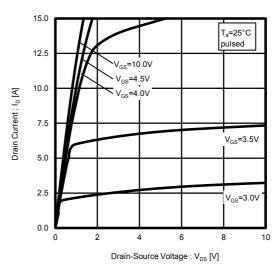


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

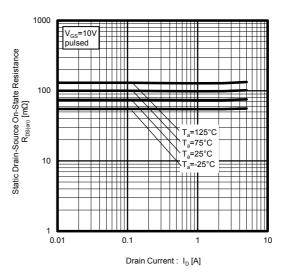


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current

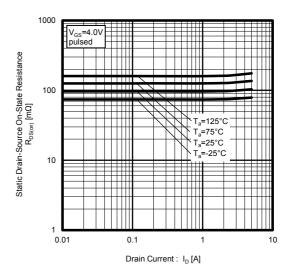
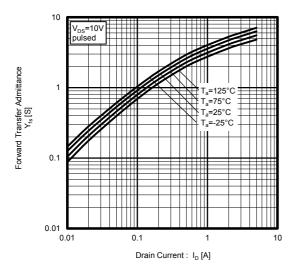
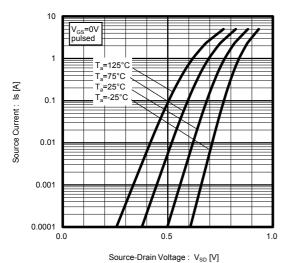


Fig.7 Forward Transfer Admittance vs. Drain Current



Fif.9 Source Current vs. Source-Drain Voltage



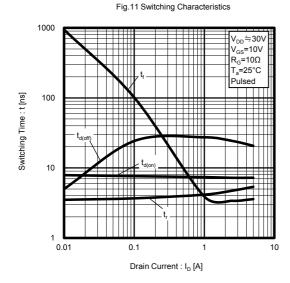


Fig.8 Typical Transfer Characteristics

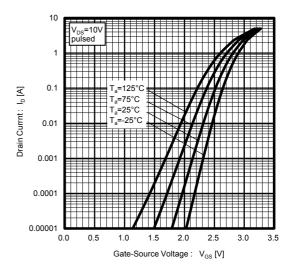


Fig.10 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

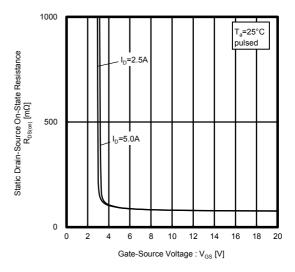


Fig.12 Dynamic Input Characteristics

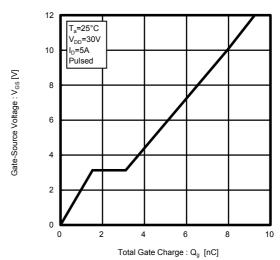


Fig.13 Typical Capacitance vs. Drain-Source Voltage

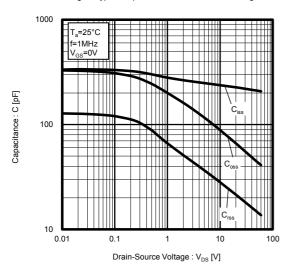


Fig.15 Normalized Transient Thermal Resistance v.s. Pulse Width

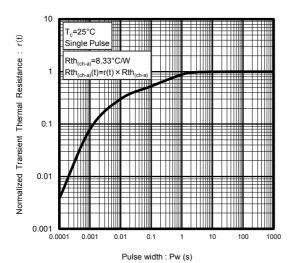
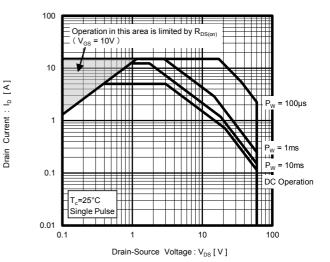


Fig.14 Maximum Safe Operating Area



Measurement circuits

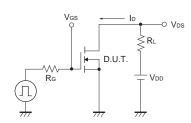


Fig.1-1 Switching Time Measurement Circuit

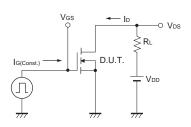


Fig.2-1 Gate Charge Measurement Circuit

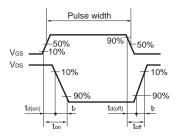


Fig.1-2 Switching Waveforms

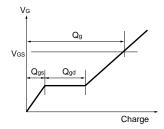


Fig.2-2 Gate Charge Waveform

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