

LVDS Interface ICs



4bit LVDS Receiver

BU90LV048 No.12057EAT03

Description

LVDS Interface IC of ROHM "Serializer" "Deserializer" operate from 8MHz to 150MHz wide clock range, and number of bits range is from 35 to 70. Data is transmitted seven times (7X) stream and reduce cable number by 3(1/3) or less. The ROHM's LVDS has low swing mode to be able to expect further low EMI.

Driver and Receiver of 4 bits operate to 250MHz. It can be used for a variety of purposes, home appliances such as LCD-TV, business machines such as decoders, instruments, and medical equipment.

Features

- 1) >500 Mbps (250 MHz) switching rates
- 2) Flow-through pinout simplifies PCB layout
- 3) 150 ps channel-to-channel skew (typical)
- 4) 100 ps differential skew (typical)
- 5) 3.7 ns maximum propagation delay
- 6) 3.3V power supply design
- 7) 6mA and 8mA selectable output drive strength
- 8) Accepts small swing (200 mV typical) differential signal levels
- 9) Supports open, short and terminated input fail-safe
- 10) Conforms to ANSI/TIA/EIA-644 Standard
- 11) Industrial temperature operating range (-40°C to +85°C)

Applications

Car Navigation System
Copier
Digital TV (Signal System)
FA equipment
Medical equipment
Vending machine, Ticket vending machine

Precaution

- ■This chip is not designed to protect from radioactivity.
- ■This document may be used as strategic technical data which subjects to COCOM regulations.

BU90LV048 Technical Note

●Block Diagram

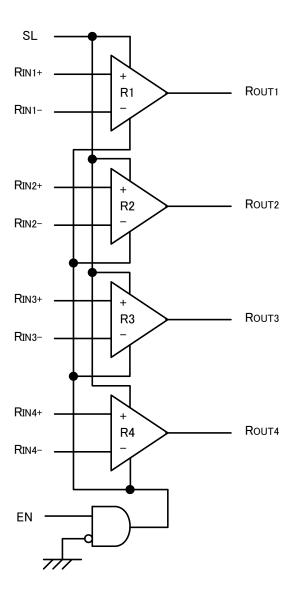


Fig.1. Block Diagram

●SSOP-B16 Package Outline and Specification

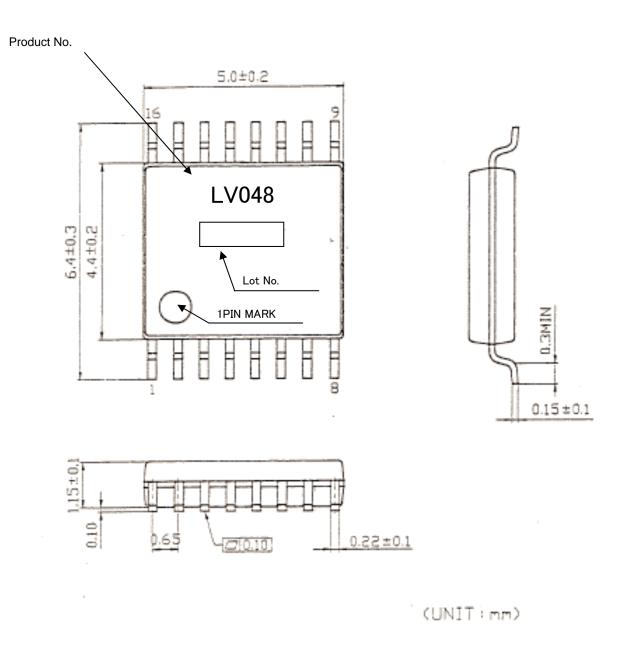


Fig.2. SSOP-B16 Package Outline and Specification

BU90LV048 Technical Note

●Pin Configuration

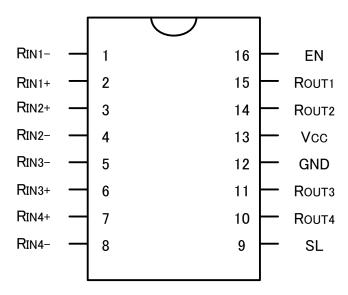


Fig.3. Pin Diagram (Top View)

●Pin Description

Table 1 : Pin Description

Pin Name	Pin No.	Туре	Descriptions
RIN+	2, 3, 6, 7	LVDS In	Non-inverting receiver input pin
RIN-	1, 4, 5, 8	LVDS In	Inverting receiver input pin
ROUT	10, 11, 14, 15	LVCMOS Out	Receiver output pin
SL	9	LVCMOS In	Drive strength select pin: When SL is low or open, Rout set 8mA mode. When SL is high, Rout set 6mA mode.
EN	16	LVCMOS In	Receiver enable pin: When EN is Low or open, the receiver is disabled. When EN is high, the receiver is enabled.
Vcc	13	Power	Power supply pin, +3.3V±0.3V
GND	12	GND	Ground pin

Function Description

		INPUT	OUTPUTS	Drive Strongth
EN	SL	R _{IN+} - R _{IN-}	R _{OUT}	Drive Strength
		VID . 0V	Н	
н	L or Open	VID0.1V	L	8mA
		Full Fail-safe OPEN/SHORT or Terminated	Н	
Н		VID . 0V	Н	
	Н	VID0.1V	L	6mA
		Full Fail-safe OPEN/SHORT or Terminated	Н	
All other combinations of EN, SL inputs		X	Z	

●Absolute Maximum Ratings

Item	Cumbal	Va	Unit		
item	Symbol	Min.	Max.	Offic	
Supply voltage	upply voltage VCC -0.3 4.0		4.0	V	
Input voltage	VIN	-0.3	VCC+0.3	V	
Output voltage	VOUT	-0.3	VCC+0.3	V	
Storage temperature range	Tstg	-55	125	°C	

●Package Power

Package	PD(mW)	DERATING(mW/°C) **1			
SSOP-B16	400	4.0			
	450 ^{*2}	4.5 ^{**2}			

%1 At temperature Ta > 25°C

%2 Package power when mounting on the PCB board.

The size of PCB board $:70 \times 70 \times 1.6 \text{ (mm}^3)$

The material of PCB board :The FR4 glass epoxy board.(3% or less copper foil area)

Recommended Operating Conditions

Item	Symbol	Value			Unit	Condition	
nem	Symbol	Min.	Тур.	Max.	Offic	Condition	
Supply voltage	Vcc	3.0	3.3	3.6	V		
Operating temperature range	Topr	-40	-	85	°C		

DC Characteristics

Parameter	Symbol	Conditions	Pin	Min	Тур	Max	Units
Differential Input High Threshold	V _{TH}	$V_{CM} = +1.2V, 0.05V,$	R _{IN+} , -		-	100	mV
Differential Input Low Threshold	V _{TL}	2.95V	R _{IN-}	-100	-	-	mV
Common-Mode Voltage Range	VCMR	VID = 200mV pk to pk		0.1	-	2.3	٧
Input Current	I _{IN}	V _{IN} = 0 or Vcc		-20	-	+20	μΑ
Output High Voltage	V _{OH1}	$I_{OH} = -8 \text{ mA}, V_{ID} = +200 \text{ mV}, \text{ SL=low}$	R _{OUT}	V _{CC} - 0.4	-	-	V
Output High Voltage	V _{OH2}	$I_{OH} = -6 \text{ mA}, V_{ID} = +200 \text{ mV}, \text{ SL= high}$		V _{CC} - 0.4	-	-	V
Output Low Voltage	V _{OL1}	$I_{OL} = 8 \text{ mA}, V_{ID} = -200 \text{ mV}, \text{ SL=low}$		-	-	0.4	V
Output Low Voltage	V _{OL2}	$I_{OL} = 6 \text{ mA}, V_{ID} = -200 \text{ mV}, SL = \text{high}$		-	-	0.4	V
Output Short Circuit Current	I _{os}	Enabled, V _{OUT} = 0V		-15	-80	-	mA
Output 3-STATE Current	I _{OZ}	Disabled, V _{OUT} = 0V or V _{CC}		-10	±1	+10	uA
Input High Voltage	V _{IH}		SL	V _{CC} × 0.8	-	V _{cc}	V
Input Low Voltage	V _{IL}		EN	GND	-	V _{CC} × 0.2	V
Input Current	l ₁	$V_{IN} = 0V$ or V_{CC} , Other Input = V_{CC} or GND		-10	-	+10	μΑ
Input Clamp Voltage	V _{CL}	I _{CL} = −18 mA		-1.5	-0.8	-	V
No Load Supply Current Receivers Enabled	I _{cc}	EN = V _{CC} , Inputs Open	V _{CC}	-	1	-	mA
No Load Supply Current Receivers Disabled	I _{CCZ}	EN= GND, SL = GND, Inputs Open		-	0.5	-	mA

Switching Characteristics

 V_{CC} = +3.3V ±0.3V, T_{opr} = -40°C to +85°C.

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Differential Propagation Delay High to Low	t _{PHLD}		1.2	2.0	3.7	ns
Differential Propagation Delay Low to High	t _{PLHD}		1.2	1.9	3.7	ns
Differential Pulse Skew t _{PHLD} - t _{PLHD}	t _{SKD1}		0	0.1	0.4	ns
Differential Channel-to-Channel Skew; same device	t _{SKD2}	$C_L = 15pF$ $V_{ID} = 200mV$	0	0.15	0.5	ns
Differential Part to Part Skew	t _{SKD3}	(Fig.4 and Fig.5)	-	-	1.0	ns
Differential Part to Part Skew	t _{SKD4}		-	-	1.5	ns
Rise Time	t _{TLH}		-	0.5	1.5	ns
Fall Time	t _{THL}		-	0.5	1.5	ns
Disable Time High to Z	t _{PHZ}		-	8	14	ns
Disable Time Low to Z	t _{PLZ}	$R_L = 2k\Omega$ $C_L = 15pF$	-	8	14	ns
Enable Time Z to High	t _{PZH}	(Fig.6 and Fig.7)	-	3	14	ns
Enable Time Z to Low	t _{PZL}		-	9	14	ns
Maximum Operating Frequency	f _{Max}	All Channels Switching	250	-	-	MHz

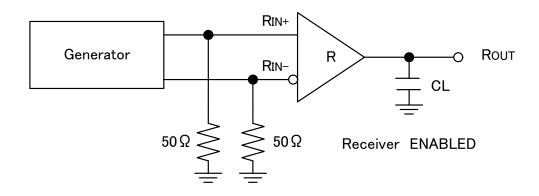


Fig.4. Receiver Propagation Delay and Transition Time Test Circuit

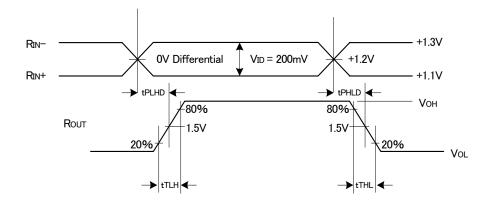


Fig.5. Receiver Propagation Delay and Transition Time Waveforms

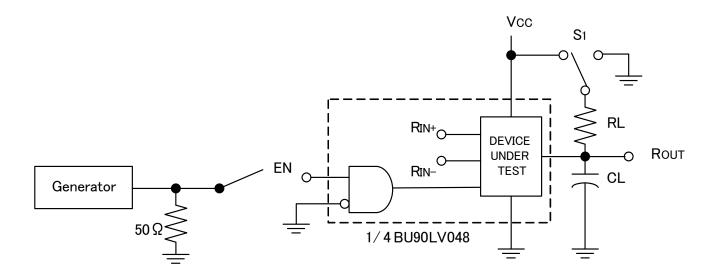


Fig.6. Receiver 3-STATE Delay Test Circuit

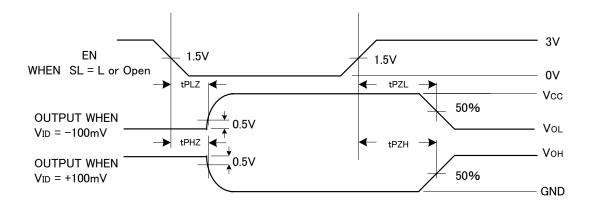


Figure 7. Receiver 3-STATE Delay Waveforms

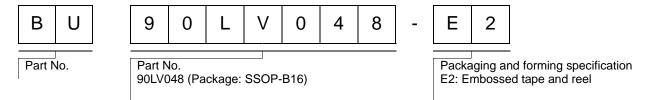
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Typical Application

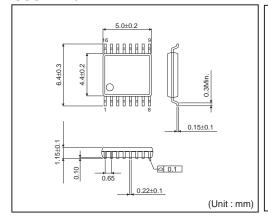


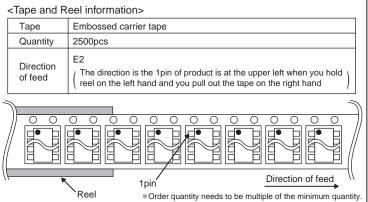
Fig.8. Point-to-Point Application

Ordering part number



SSOP-B16





Notes

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