BD179

Plastic Medium-Power NPN Silicon Transistor

This device is designed for use in 5.0 to 10 Watt audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

Features

- DC Current Gain h_{FE} = 40 (Min) @ I_C = 0.15 Adc
- BD179 is complementary with BD180
- Pb-Free Package is Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	80	Vdc
Collector-Base Voltage	V _{CBO}	80	Vdc
Emitter-Base Voltage	V _{EBO}	5.0	Vdc
Collector Current	Ic	3.0	Adc
Base Current	Ι _Β	1.0	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	30 240	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	θЈС	4.16	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



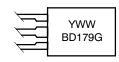
ON Semiconductor®

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3.0 AMPERES POWER TRANSISTORS NPN SILICON 80 VOLTS, 30 WATTS



MARKING DIAGRAM



Y = Year

WW = Work Week

BD179 = Device Code

G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
BD179	TO-225	500 Units/Box
BD179G	TO-225 (Pb-Free)	500 Units/Box

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Sustaining Voltage (Note 1) $(I_C = 0.1 \text{ Adc}, I_B = 0)$	V _(BR) CEO	80	-	Vdc
Collector Cutoff Current (V _{CB} = 80 Vdc, I _E = 0)	I _{CBO}	-	0.1	mAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)	I _{EBO}	_	1.0	mAdc
DC Current Gain (I _C = 0.15 A, V _{CE} = 2.0 V) (I _C = 1.0 A, V _{CE} = 2.0 V)	h _{FE}	63 15	160 -	-
Collector-Emitter Saturation Voltage (Note 1) (I _C = 1.0 Adc, I _B = 0.1 Adc)	V _{CE(sat)}	-	0.8	Vdc
Base-Emitter On Voltage (Note 1) (I _C = 1.0 Adc, V _{CE} = 2.0 Vdc)	V _{BE(on)}	-	1.3	Vdc
Current-Gain - Bandwidth Product (I _C = 250 mAdc, V _{CE} = 10 Vdc, f = 1.0 MHz)	f _T	3.0	-	MHz

^{1.} Pulse Test: Pulse Width ≤ 300 As, Duty Cycle ≤ 2.0%.

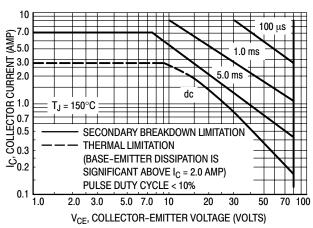


Figure 1. Active Region Safe Operating Area

The Safe Operating Area Curves indicate I_C – V_{CE} limits below which the device will not enter secondary breakdown. Collector load lines for specific circuits must fall within the applicable Safe Area to avoid causing a catastrophic failure. To insure operation below the maximum T_J , power-temperature derating must be observed for both steady state and pulse power conditions.

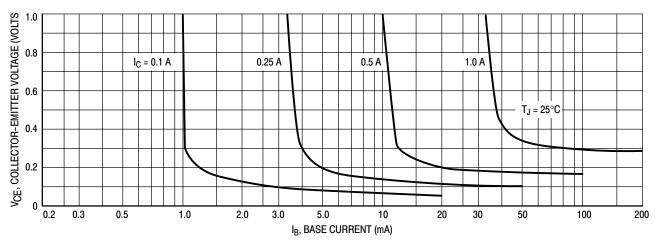
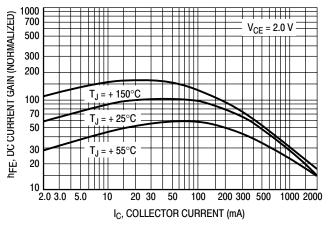


Figure 2. Collector Saturation Region

1.5

1.2

 $T_{.l} = 25^{\circ}C$



0.9

V_{BE(sat)} @ I_C/I_B = 10

V_{BE} @ V_{CE} = 2.0 V

V_{CE(sat)} @ I_C/I_B = 10

2.0 3.0 5.0 10 20 30 50 100 200 300 500 1000 2000 I_C, COLLECTOR CURRENT (mA)

Figure 3. Current Gain

Figure 4. "On" Voltages

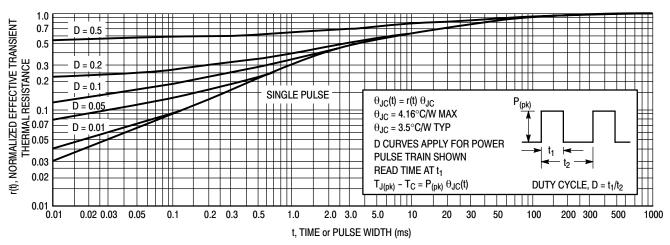
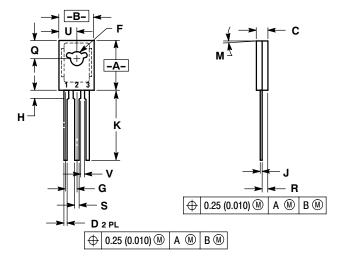


Figure 5. Thermal Response

BD179

PACKAGE DIMENSIONS

TO-225 CASE 77-09 **ISSUE Z**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 3. 077-01 THRU -08 OBSOLETE, NEW STANDARD 077-09.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.425	0.435	10.80	11.04	
В	0.295	0.305	7.50	7.74	
С	0.095	0.105	2.42	2.66	
D	0.020	0.026	0.51	0.66	
F	0.115	0.130	2.93	3.30	
G	0.094	BSC	2.39	BSC	
Н	0.050	0.095	1.27	2.41	
J	0.015	0.025	0.39	0.63	
K	0.575	0.655	14.61	16.63	
M	5° TYP		5° TYP		
Q	0.148	0.158	3.76	4.01	
R	0.045	0.065	1.15	1.65	
S	0.025	0.035	0.64	0.88	
U	0.145	0.155	3.69	3.93	
٧	0.040		1.02		

STYLE 1:

PIN 1. EMITTER 2. COLLECTOR

BASE

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