

SiGe: C LOW NOISE AMPLIFIER FOR GPS/MOBILE COMMUNICATIONS

DESCRIPTION

The μ PC8233TK is a silicon germanium carbon (SiGe:C) monolithic integrated circuit designed as low noise amplifier for GPS and mobile communications. This device exhibits low noise figure and high power gain characteristics. This device is enabled in the frequency range from 1.5 to 2.4 GHz by modifying the external matching circuit.

This device is suitable for the reduction in power consumption of the mobile communication system because it operates by low voltage and low current.

The package is 6-pin lead-less minimold, suitable for surface mount.

This IC is manufactured using our UHS4 (<u>U</u>Itra <u>High Speed Process</u>) SiGe:C bipolar process.

FEATURES

Supply voltage : Vcc = 1.6 to 3.3 V (2.7 V TYP.)

• Low noise : NF = 0.90 dB TYP. @ Vcc = 2.7 V, fin = 1.575 MHz

NF = 0.90 dB TYP. @ Vcc = 1.8 V, fin = 1575 MHz

• High gain : $G_P = 20 \text{ dB TYP}$. @ $V_{CC} = 2.7 \text{ V}$, $f_{in} = 1575 \text{ MHz}$

 $G_P = 19.5 \text{ dB TYP.} @ Vcc = 1.8 \text{ V, fin} = 1575 \text{ MHz}$

Low current consumption : Icc = 3.5 mA TYP. @ Vcc = 2.7 V

Built-in power-saving function
 VPSon = 1.0 V to Vcc, VPSoff = 0.0 to 0.4 V

• High-density surface mounting : 6-pin lead-less minimold package (1.5 \times 1.1 \times 0.55 mm)

Included very robust bandgap regulator (Small Vcc and TA dependence)

Included protection circuits for ESD

APPLICATION

· Low noise amplifier for GPS and mobile communications

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPC8233TK-E2	μPC8233TK-E2-A	6-pin lead-less minimold (1511 PKG) (Pb-Free)	6P	8 mm wide embossed taping Pin 1, 6 face the perforation side of the tape Qty 5 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.

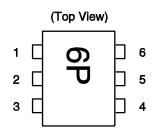
Part number for sample order: µPC8233TK-A

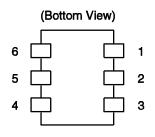
Caution: Observe precautions when handling because these devices are sensitive to electrostatic discharge

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

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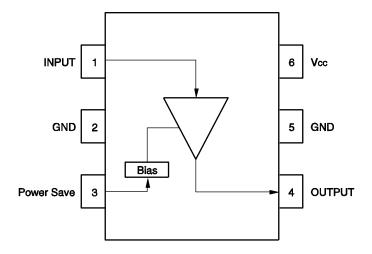
PIN CONNECTIONS





Pin No.	Pin Name	
1	INPUT	
2	GND	
3	Power Save	
4	OUTPUT	
5	GND	
6	Vcc	

INTERNAL BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Conditions	Ratings	Unit
Supply Voltage	Vcc	TA = +25°C	4.0	V
Power-Saving Voltage	Vps	TA = +25°C	4.0	V
Power Dissipation	Po	T _A = +85°C Not e	232	mW
Operating Ambient Temperature	TA		-40 to +85	°C
Storage Temperature	Tstg		-55 to +150	°C
Input Power	Pin		+10	dBm

Note~ Mounted on double-side copper-clad $50\times50\times1.6$ mm epoxy glass PWB

RECOMMENDED OPERATING RANGE

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	Vcc	1.6	2.7	3.3	V
Operating Ambient Temperature	TA	-40	+25	+85	°C
Power Save Turn-on Voltage	VPSon	1.0	-	Vcc	V
Power Save Turn-off Voltage	VPSoff	0	-	0.4	V

ELECTRICAL CHARACTERISTICS

(TA = +25°C, Vcc = Vps = 2.7 V, fin = 1 575 MHz, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	Icc	No Signal (VPS = 2.7 V)	2.5	3.5	4.8	mA
		At Power-Saving Mode (VPS = 0 V)	-	-	1	μΑ
Power Gain	G₽	Pin = -35 dBm	17.5	20.0	22.5	dB
Noise Figure	NF		-	0.9	1.2	dB
Input Return Loss	RLin		7	10	-	dB
Output Return Loss	RLout		10	16	-	dB

STANDARD CHARACTERISTICS FOR REFERENCE 1

(TA = +25°C, Vcc = VPS = 2.7 V, fin = 1 575 MHz, unless otherwise specified)

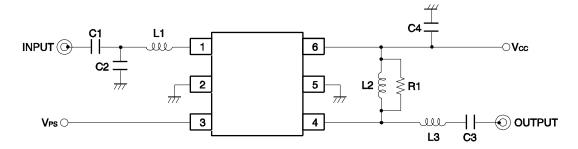
Parameter	Symbol	Test Conditions	Reference	Unit
Input 3rd Order Intercept Point	IIP3	fin1 = 1 575 MHz, fin2 = 1 574 MHz	-8.5	dBm
Isolation	ISL		36	dB
Gain 1 dB Compression Input Power	Pin (1 dB)		-23	dBm

STANDARD CHARACTERISTICS FOR REFERENCE 2

(TA = +25°C, Vcc = VPS = 1.8 V, fin = 1 575 MHz, unless otherwise specified)

Parameter	Symbol	Test Conditions	Reference	Unit
Circuit Current	Icc	No Signal (VPS = 1.8 V)	3.3	mA
Power Gain	G₽	Pin = -35 dBm	19.5	dB
Noise Figure	NF		0.9	dB
Input 3rd Order Intercept Point	IIP3	fin1 = 1 575 MHz, fin2 = 1 574 MHz	-9.5	dBm
Input Return Loss	RLin		9.5	dB
Output Return Loss	RLout		15.5	dB
Isolation	ISL		36	dB
Gain 1 dB Compression Input Power	Pin (1 dB)		-23.5	dBm

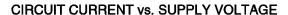
TEST CIRCUIT

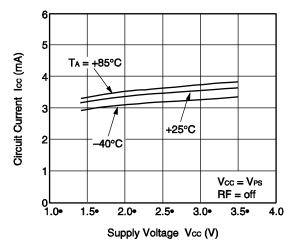


COMPONENT LIST

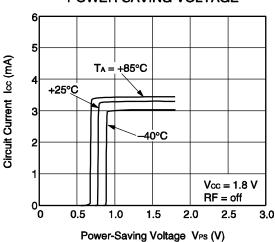
Symbol	Туре	Value	Unit
C1	Chip Capacitor	1 000	pF
C2	Chip Capacitor	1.2	pF
C3	Chip Capacitor	18	pF
C4	Chip Capacitor	1 000	pF
L1	Chip Inductor	8.2	nΗ
L2	Chip Inductor	18	nΗ
L3	Chip Inductor	6.8	nΗ
R1	Chip Resistor	360	Ω

TYPICAL CHARACTERISTICS (T_A = +25°C, unless otherwise specified)

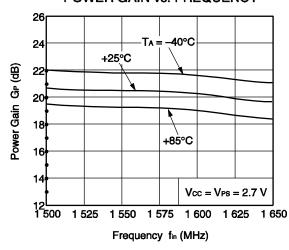




CIRCUIT CURRENT vs. POWER-SAVING VOLTAGE

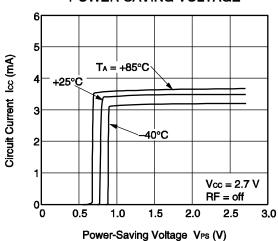


POWER GAIN vs. FREQUENCY

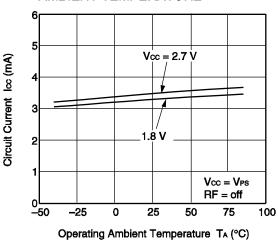


Remark The graphs indicate nominal characteristics.

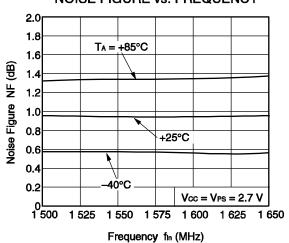
CIRCUIT CURRENT vs. POWER-SAVING VOLTAGE

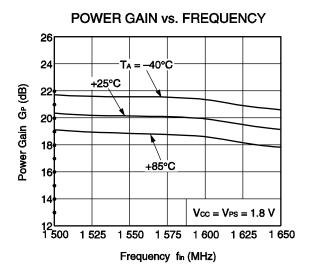


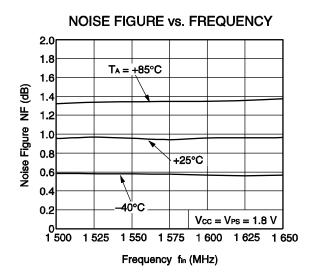
CIRCUIT CURRENT vs. OPERATING AMBIENT TEMPERATURE

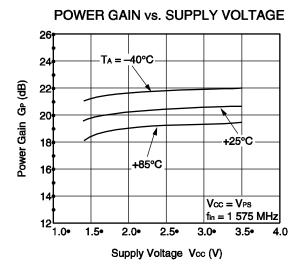


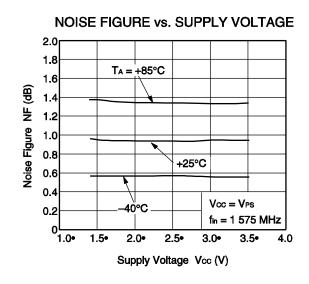
NOISE FIGURE vs. FREQUENCY

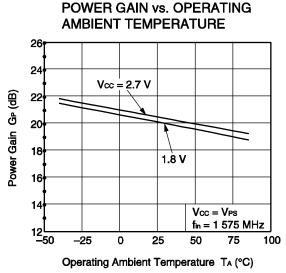


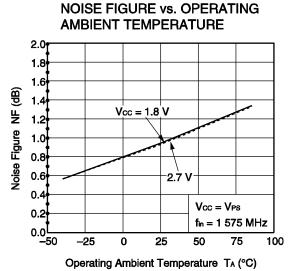




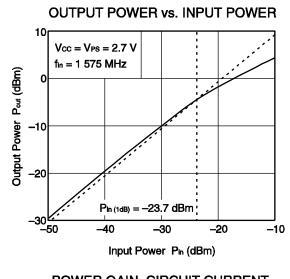


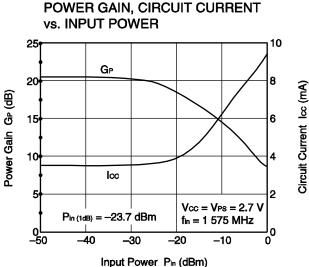


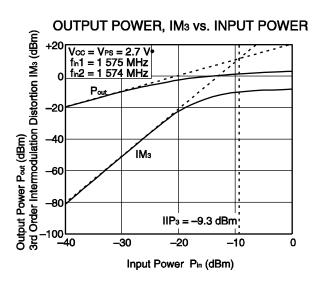




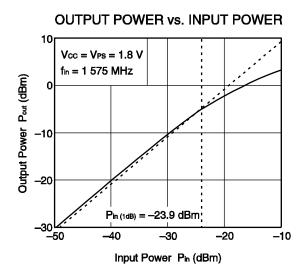
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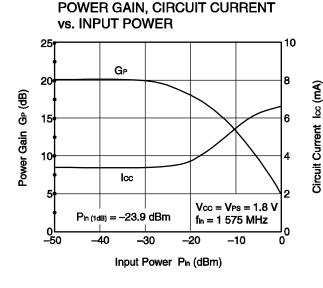


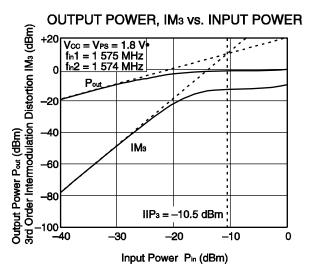


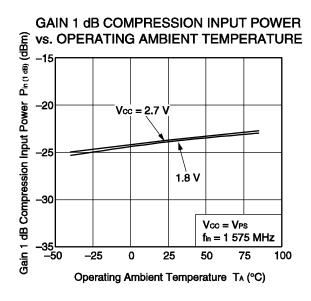


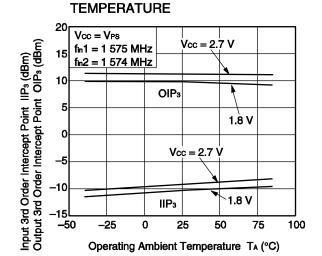




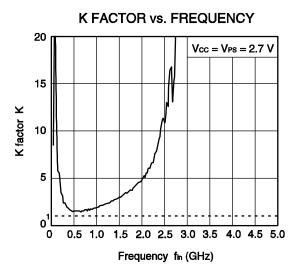


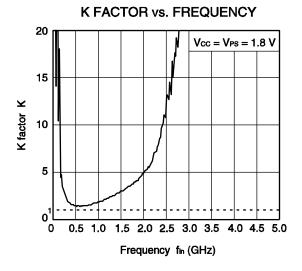






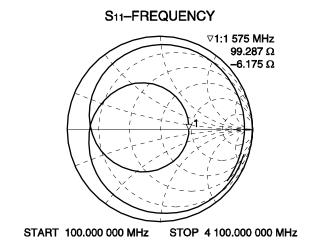
IIP3, OIP3 vs. OPERATING AMBIENT

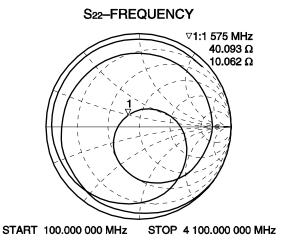


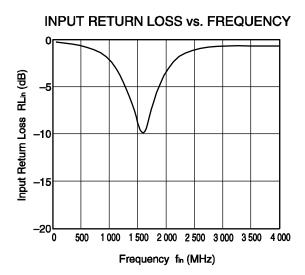


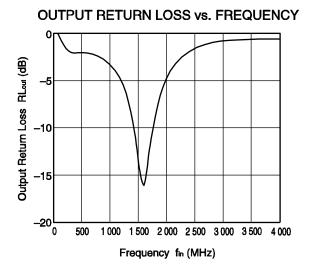
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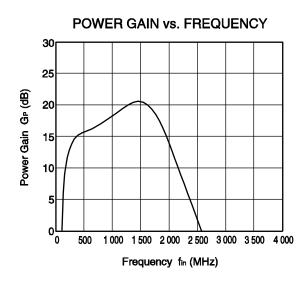
S-PARAMETERS (TA = +25°C, Vcc = VPS = 2.7 V, monitored at connector on board)

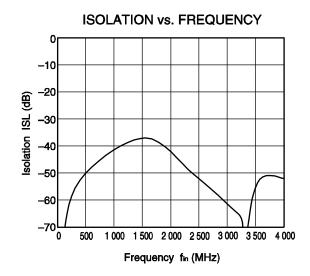






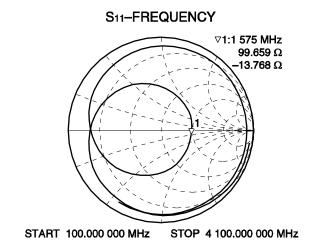


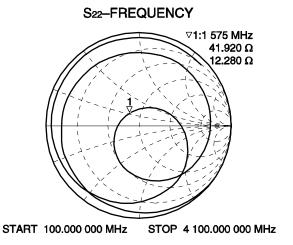


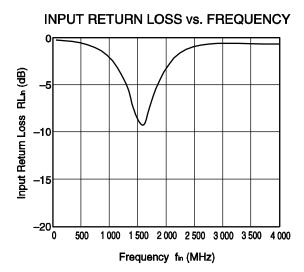


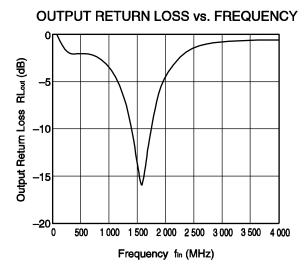
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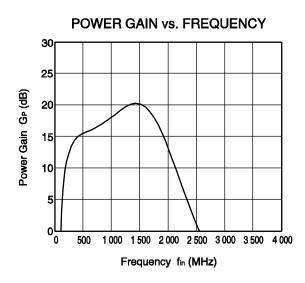
S-PARAMETERS (TA = +25°C, Vcc = VPS = 1.8 V, monitored at connector on board)

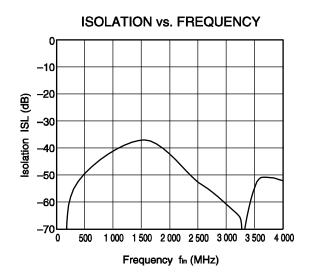








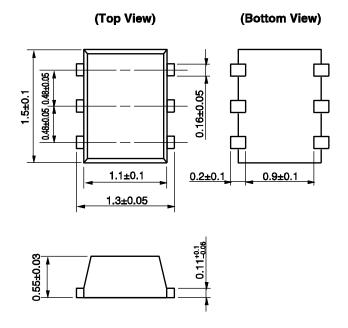




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PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (1511 PKG) (UNIT: mm)



NOTES ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation).
 All the ground terminals must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to Vcc line.
- (4) Do not supply DC voltage to INPUT pin.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature	: 260°C or below : 10 seconds or less	IR260
	Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).