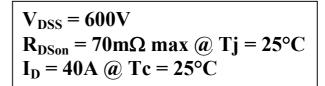
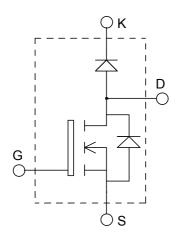


ISOTOP® Boost chopper Super Junction MOSFET Power Module





Application

- AC and DC motor control
- Switched Mode Power Supplies
- Power Factor Correction
- Brake switch

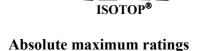
Features



- Power Semiconductors
- Ultra low R_{DSon}Low Miller capacitance
- Ultra low gate charge
- Avalanche energy rated
- ISOTOP® Package (SOT-227)
- Very low stray inductance
- High level of integration



- Outstanding performance at high frequency operation
- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive T_C of V_{CEsat}
- RoHS Compliant



Symbol	Parameter			Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage			600	V
Ţ	Continuous Drain Current $ \frac{T_c = 25^{\circ}C}{T_c = 80^{\circ}C} $		40		
I_D			30	Α	
I_{DM}	Pulsed Drain current			120	
V_{GS}	Gate - Source Voltage			±20	V
R_{DSon}	Drain - Source ON Resistance			70	mΩ
P_{D}	Maximum Power Dissipation		$T_c = 25$ °C	290	W
I_{AR}	Avalanche current (repetitive and non repetitive)			20	Α
E_{AR}	Repetitive Avalanche Energy			1	mJ
E_{AS}	Single Pulse Avalanche Energy			1800	1113
IF_{AV}	Maximum Average Forward Current	Duty cycle=0.5	$Tc = 80^{\circ}C$	30	A
IF_{RMS}	RMS Forward Current (Square wave, 5	50% duty)	•	39	А

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

1 - 9



All ratings @ $T_j = 25$ °C unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 25^{\circ}C$			25	μА
		$V_{GS} = 0V, V_{DS} = 600V$ $T_j = 125^{\circ}C$			250	
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 20A$			70	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{mA}$	2.1	3	3.9	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		7015		
C_{oss}	Output Capacitance	$V_{DS} = 25V$		2565		pF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		212		
Q_{g}	Total gate Charge	$V_{GS} = 10V$		259		
Q_{gs}	Gate – Source Charge	$V_{Bus} = 300V$		29		nC
Q_{gd}	Gate – Drain Charge	$I_D = 40A$		111		
$T_{d(on)}$	Turn-on Delay Time	Resistive Switching		20		
$T_{\rm r}$	Rise Time	$V_{GS} = 15V$ $V_{Bus} = 380V$		30		n .c
$T_{d(off)}$	Turn-off Delay Time	$I_{\rm D} = 40A$		115		ns
T_{f}	Fall Time	$R_G = 1.8\Omega$		10		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		670		1
Eoff	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 400V$ $I_D = 40A, R_G = 5\Omega$		980		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		1100		
Eoff	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 400V$ $I_D = 40A, R_G = 5\Omega$		1206		μJ



Chopper diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit	
V_{F}	Diode Forward Voltage	$I_F = 30A$			1.6	1.8		
		$I_F = 60A$			1.9		V	
		$I_F = 30A$	$T_i = 125$ °C		1.4			
I_{RM}	Maximum Reverse Leakage Current	$V_{R} = 600V$	$T_j = 25$ °C			250	μA	
1RM		$V_{R} = 600V$	$T_j = 125$ °C			500		
C_{T}	Junction Capacitance	$V_{R} = 200V$			44		pF	
	Reverse Recovery Time	$I_F=1A, V_R=30V$ di/dt =100A/\(\mu\)s	$T_j = 25$ °C		23		ns	
t_{rr}	Reverse Recovery Time	$T_i = 25^{\circ}C$ $T_i = 125^{\circ}C$ $T_i = 125^{\circ}C$ $T_i = 25^{\circ}C$ $T_i = 125^{\circ}C$ $T_i = 125^{\circ}C$	$T_i = 25^{\circ}C$		85			
				160				
I_{RRM}	Maximum Reverse Recovery Current			4		Α		
1RRM				8		Λ		
0	Reverse Recovery Charge	$di/dt = 200A/\mu s$	$T_j = 25$ °C		130		пC	
Q _{rr}			$T_{j} = 125^{\circ}C$		700		IIC	
t_{rr}	Reverse Recovery Time	$I_F = 30A$ $V_R = 400V$ $di/dt = 1000A/\mu s$			70		ns	
Q_{rr}	Reverse Recovery Charge		$T_j = 125$ °C		1300		nC	
I_{RRM}	Maximum Reverse Recovery Current				30		A	

Thermal and package characteristics

Symbol	Characteristic		Min	Тур	Max	Unit
R_{thJC}	Junction to Case Thermal Resistance	CoolMos			0.43	
		Diode			1.21	°C/W
R_{thJA}	Junction to Ambient (IGBT & Diode)				20	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz		2500			V
T_{J}, T_{STG}	Storage Temperature Range		-55		150	°C
$T_{ m L}$	Max Lead Temp for Soldering:0.063" from case for 10 sec				300	C
Torque	Mounting torque (Mounting = 8-32 or 4mm Machine and terminals = 4mm Machine)				1.5	N.m
Wt	Package Weight			29.2		g

Typical CoolMOS Performance Curve

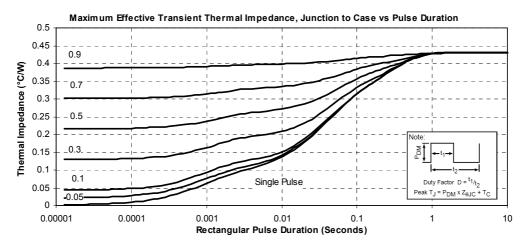
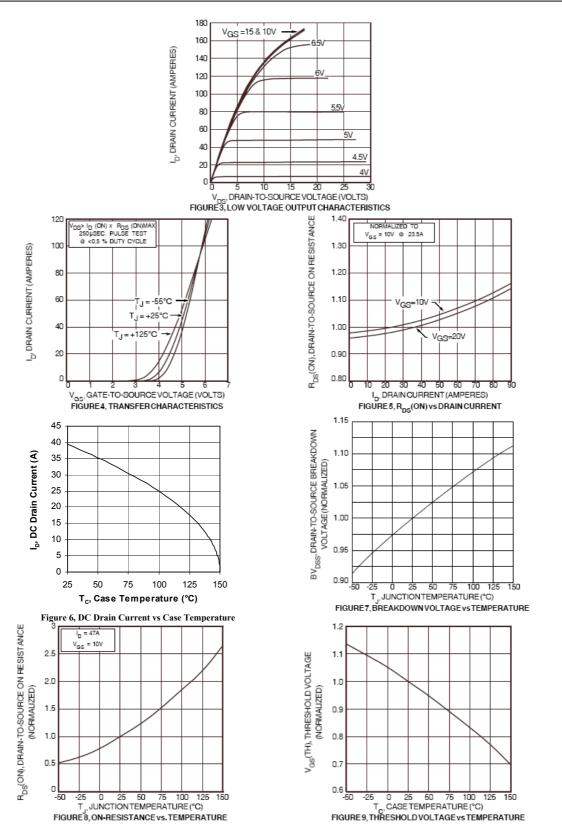
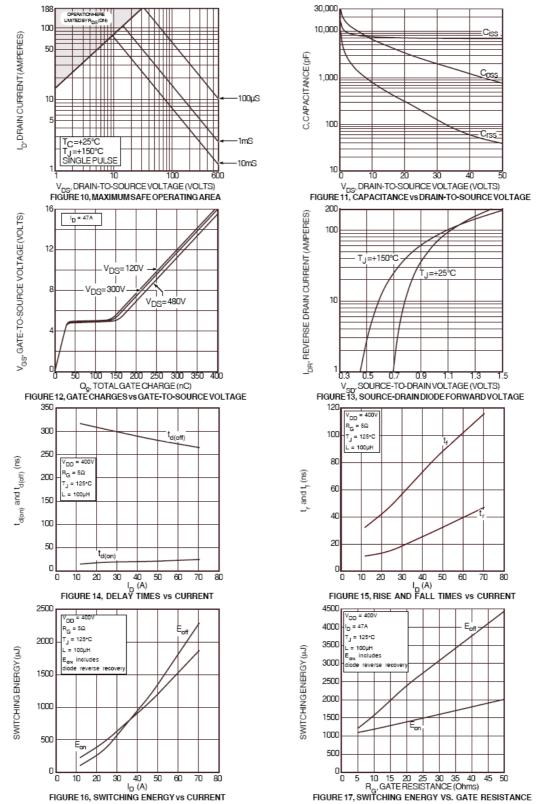


Fig 1, Maximum Effective transient thermal Impedance, Junction to case vs Pulse Duration











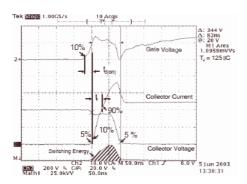


Figure 18, Turn-on Switching Waveforms and Definitions

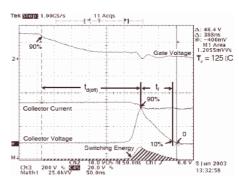


Figure 19, Turn-off Switching Waveforms and Definitions

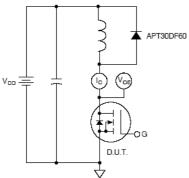
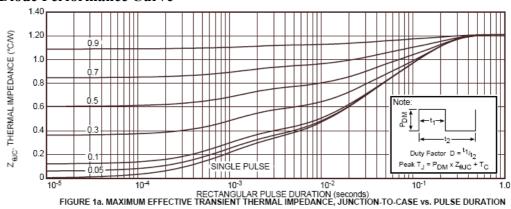


Figure 20, Inductive Switching Test Circuit

Typical Diode Performance Curve



Power (watts)

0.320 °C/W

0.00278 J/°C

0.00278 J/°C

0.0421 J/°C

0.375 °C/W

0.242 J/°C

FIGURE 1b, TRANSIENT THERMAL IMPEDANCE MODEL



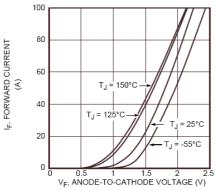


Figure 2. Forward Current vs. Forward Voltage

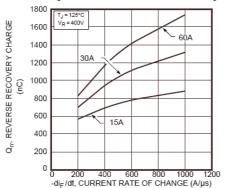


Figure 4. Reverse Recovery Charge vs. Current Rate of Change

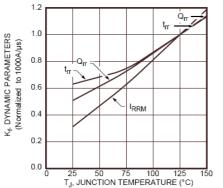


Figure 6. Dynamic Parameters vs. Junction Temperature

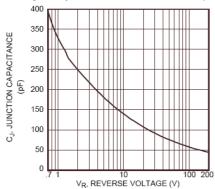


Figure 8. Junction Capacitance vs. Reverse Voltage

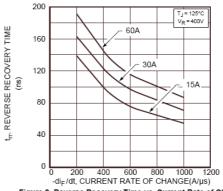


Figure 3. Reverse Recovery Time vs. Current Rate of Change

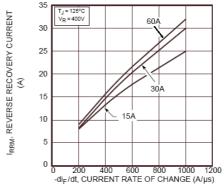


Figure 5. Reverse Recovery Current vs. Current Rate of Change

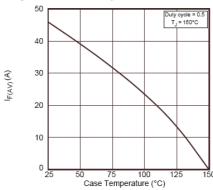


Figure 7. Maximum Average Forward Current $\emph{vs.}$ CaseTemperature



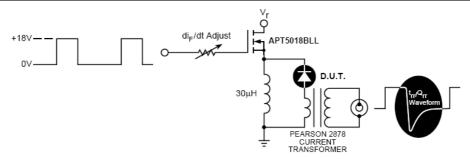
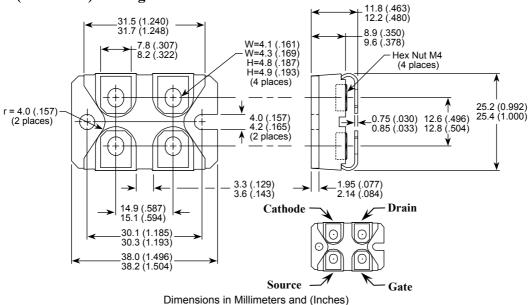


Figure 9. Diode Test Circuit

- I_F Forward Conduction Current
 di_F/dt Rate of Diode Current Change Through Zero Crossing.
 I_{RRM} Maximum Reverse Recovery Current.
 t_{rr} Reverse Recovery Time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through I_{RRM} and 0.25 I_{RRM} passes through zero.
- **5** $Q_{\Gamma\Gamma}$ Area Under the Curve Defined by I_{RRM} and $t_{\Gamma\Gamma}$

Figure 10, Diode Reverse Recovery Waveform and Definitions

SOT-227 (ISOTOP®) Package Outline



"COOLMOSTM comprise a new family of transistors developed by Infineon Technologies AG. "COOLMOS" is a trademark of Infineon Technologies AG".

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