


In-Circuit Emulator MN101C51

Product Name	Product Number	Note
ICE	PX-ICE101C/D	In-Circuit Emulator Specification -> PX-ICE101C/D
Probe set	PX-PRB101C51-L00*	For other product type supported by this board, refer to the instruction.
EV board	PRB-EV101C15	
Adapter board	PRB-ADP101C28/51(80PIN)	
ICE CONNECTOR	PRB-TET80LF14-SN-YQSET	Product of Yamaichi Electronics Drawing
Interface	PX-IFC-PCC-6	Compliant with PCMCIA Ver2.1/JEIDA Ver4.2
	PC-IFC-PCI-6	Compliant with PCI2.1 of PCI-SIG standard. When using the Low profile the PCI with small-footprint PC's, replace the bracket by provided one.
Debugger	PX-SDX101C00-0P0*	PanaX Series Debugger
	PX-DBF101C00-0P0*	Debug Factory® Builder
C Compiler/ Assembler	PX-ICC101C00-0P0*	

	
<p>PX-ICE101C/D</p>	<p>PX-PRB101C51-L00*</p>
	
<p>PRB-TET80LF14-SN-YQSET</p>	<p>PX-IFC-PCC-6</p>
	
<p>PX-IFC-PCI-6</p>	

PRB-EV101C15

<Compatible devices>

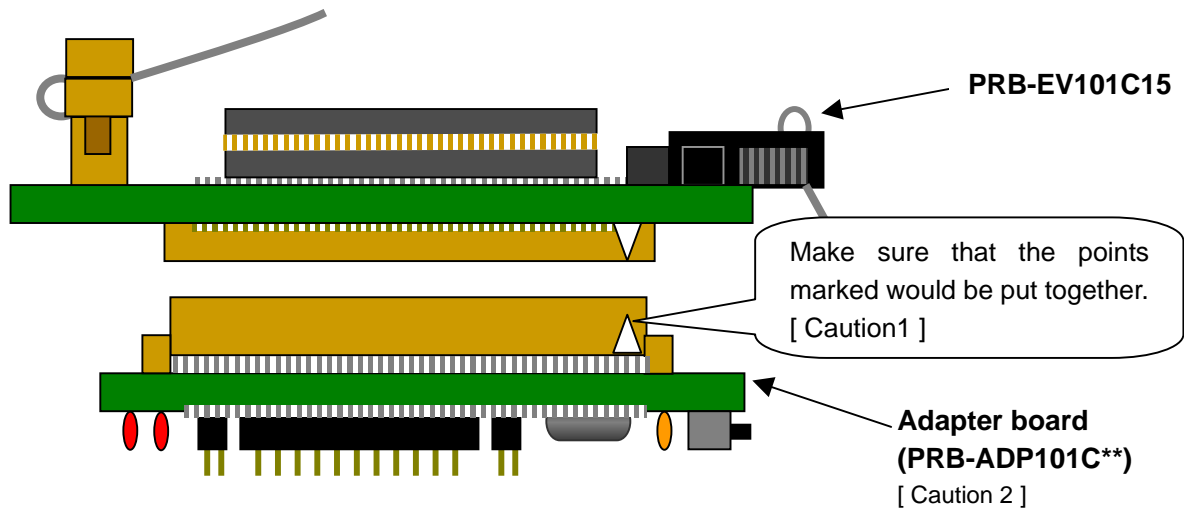
-This board corresponds to the following devices.

(The product type is subject to change without prior notice. The latest information should be confirmed on our web site.)

-MN101C08	-MN101C16	-MN101C39
-MN101C09	-MN101C24	-MN101C42
-MN101C10	-MN101C27	-MN101C45
-MN101C11	-MN101C28	-MN101C48
-MN101C14	-MN101C30	-MN101C51
-MN101C15	-MN101C38	-MN101C94

< How to connect >

Figure 1. Connecting a PRB-EV101C15 to an Adapter board(PRB-ADP101C**)



[Caution1]

When connect the boards, make sure that they are connected without tilt.

If you put pressure on one side of the board, that may cause any damage to the pins.

[Caution2]

Please visit our web site to check the adapter boards corresponding to your microcomputer .we update the web site periodically.

PRB-ADP101C28/51(80PIN) Probe Switches

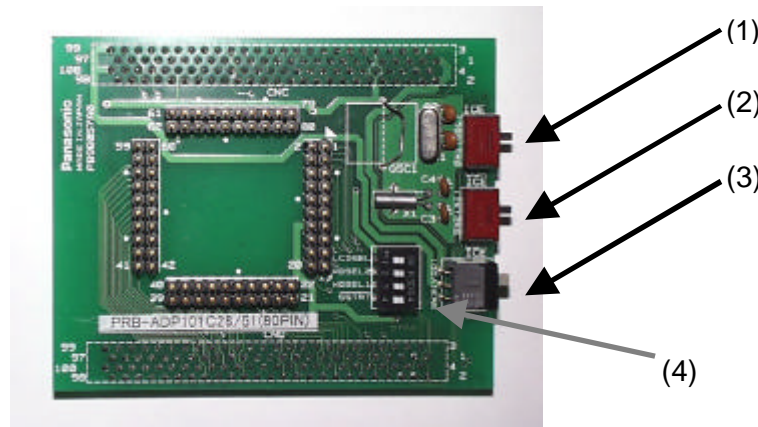
Adapter boards vary depending upon the models. This adapter board must be used for only MN101C28/51(80PIN).

Use the adapter board with an EV board, PRB-EV101C15.

Improper matching may cause any damage to the ICE.

The switches that the adapter board provides for configuring the probe are described below.

Adapter Board Layout



(1) Oscillator control (SW1)

Set this switch to its USR position to drive the in-circuit emulator with the oscillator built into the target device. If there is no target device, set this switch to the ICE position to use the oscillator built into the probe.

(2) X1 control (SW2)

Set this switch to its USR position to drive the in-circuit emulator with the X1 oscillator built into the target device. If there is no target device, set this switch to the ICE position to use the oscillator built into the probe.

(3) Power supply control (SW3)

Set this switch to its USR position to use the power supply from the target device. If there is no target device, set this switch to the ICE position to use the 5-volt power supply from the in-circuit emulator.

(4) Function control DIP switches

These switch settings vary with the individual target device as described below.

LCDSEL ON: if using LCD
 OFF: if using LED

Watchdog Timer Frequency (WDSEL2, WDSEL1)

Switch settings		Watchdog timer Frequency
WDSEL1	WDSEL2	
OFF	OFF	$f_{osc}/2^{17}$
ON	OFF	$f_{osc}/2^{19}$
Don't care	ON	$f_{osc}/2^{21}$

Starting Oscillation, after a reset (\overline{SSTRT})

ON: Low-speed (X1) operation

OFF: High-speed (OSC)

1 In-circuit Emulator Specifications

1-1 Functional specifications

Item		Specifications
Devices	MN101CXX Series	
Memory size	Emulation memory	256 Kbytes (standard) 480 Kbytes (maximum)
Break function	ROM break RAM break Sequential break Trace break RAM access break External break	Maximum 4 events Condition: Area and pass count specification Maximum 4 events Condition: Specifications of area and pass count, bit mask, read/write/access, match/mismatch, AND condition 2-level 1-bit
Trace function	Trace memory size Trace get data Trace mode	511 steps (standard) ROM address, RAM address, RAM data, R/W Normal mode, ROM/RAM area mode, delayed trigger mode
Timer function	Measurement mode Time measurement resolution	Execution time measurement mode, maximum execution time measurement mode 100ns
Trigger output function	Trigger output	One
RAM monitor function	Sample memory Display mode	32 bytes Dump list mode, bit map mode
Performance measurement	Profile measurement	Run ratio (%) display
Clock	OSCI XI	Emulator and target Emulator and target

1-2 Electrical specifications

Parameter	Rating
Emulator and probe supply voltage	0.5 to 6.0V
EXT. BREAK input voltage	-0.3 to 5.5V
Trigger output voltage	-0.3 to 5.5V
Trigger output current	±10mA

1-3 Environmental specifications

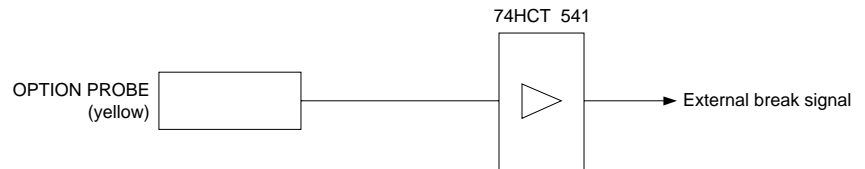
Parameter	Rating
Operating temperature	10°C to 30°C
Storage temperature	0°C to 45°C
Operating humidity	20% to 80%
Storage humidity	90% or less

1-4 External dimensions

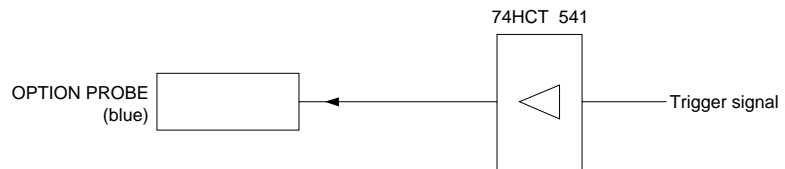
Length × width × height	130 × 100 × 40mm
-------------------------	------------------

1-5 Target interface

External break probe input (OPTION PROBE)

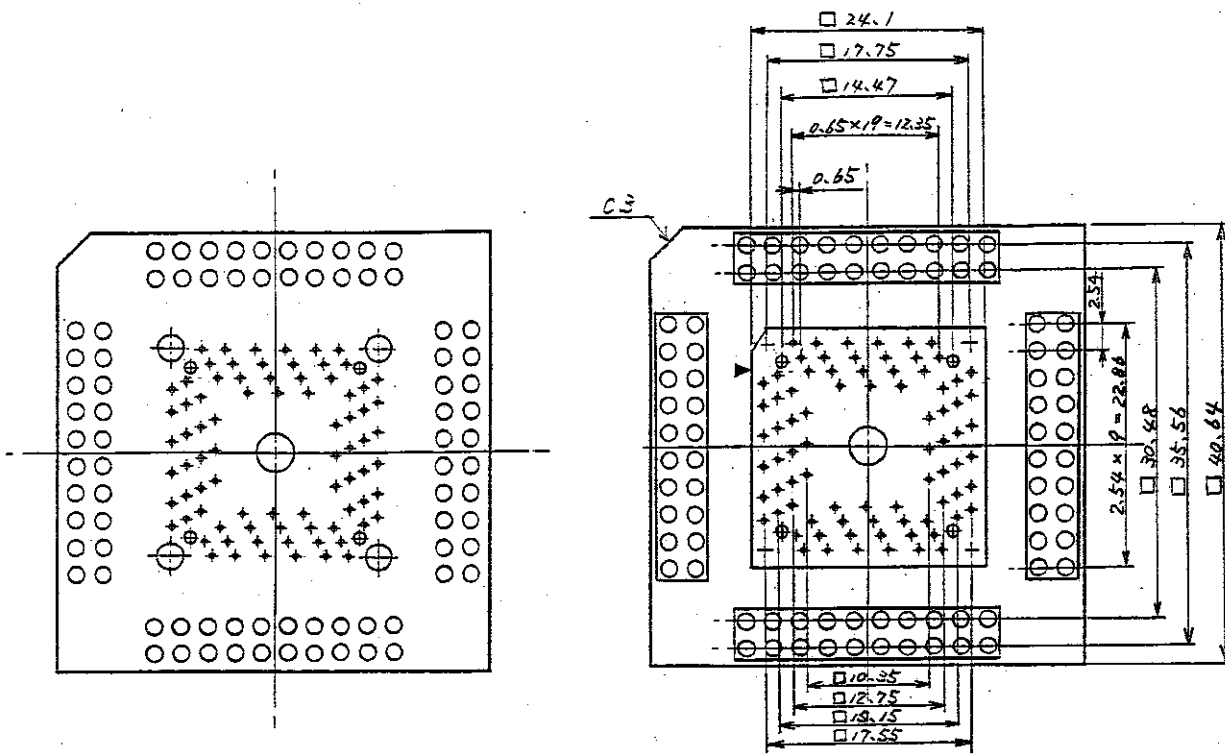


Trigger output (OPTION PROBE)

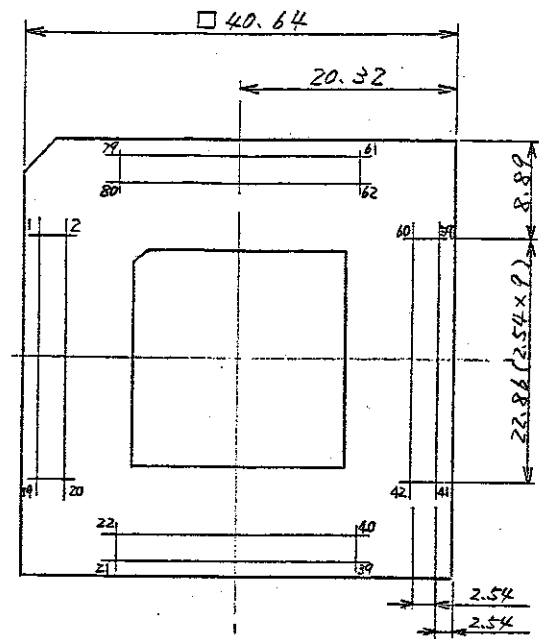


インサーキットエミュレータ MN101C51

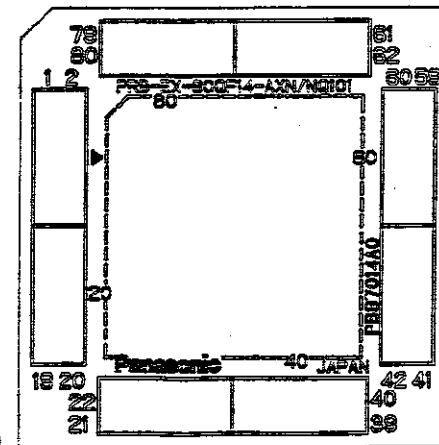
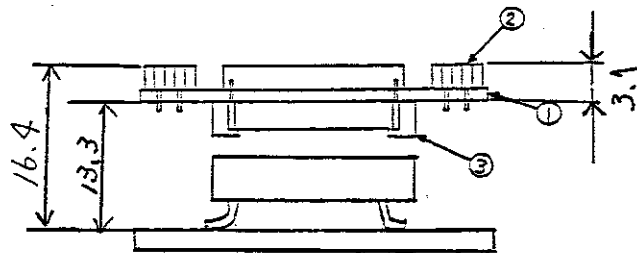
商品名	型番	備考
ICE 本体	PX-ICE101C/D	<ul style="list-style-type: none"> 機能仕様→ PX-ICE101C/D PX-ICE101C/D は PGA プローブとセットでの販売となります。
プローブセット	PX-PRB101C51-L00*	<ul style="list-style-type: none"> 取扱説明書には対応している他品種が掲載されていますのでご覧ください。 プローブのみを購入する場合は、該当品種の情報を参照して下さい。
エバボード	PRB-EV101C15	
アダプタボード	PRB-ADP101C28/51(80PIN)	
延長アダプタ	PRB-TET80LF14-SN-YQSET	
インタフェース	PX-IFC-PCC-6	<ul style="list-style-type: none"> PCMCIA Ver2.1/JEIDA Ver4.2 準拠
	PC-IFC-PCI-6	<ul style="list-style-type: none"> PCI-SIG 規定の PCI2.1 準拠 省スペース型 PC の Low Profile PCI の場合は別金具で対応
デバッガ	PX-SDX101C00-0P0*	<ul style="list-style-type: none"> PanaX Series Debugger
	PX-DBF101C00-0P0*	<ul style="list-style-type: none"> Debug Factory®Builder
C コンパ イラゲセンブラ	PX-ICC101C00-0P0*	



外形寸法図



外形寸法図



部品面シルク図 (対面)

3	コネクタ	1	YQPACK080SB
2	ソケット	1	415-91-264-41-001 (プレシディオブ)
1	基板	1	基板名 (PB97014AD)
N0	部品名	数量	備考

製図図面	
作成日	1999年3月26日