

XILINX AEROSPACE & DEFENSE SOLUTIONS

		Defense-Grade FPGAs				
		Spartan®-6Q FPGAs				
		Part Number	XQ6SLX75	XQ6SLX150	XQ6SLX75T	XQ6SLX150T
Logic Resources	Logic Cells		75,000	147,000	75,000	147,000
	CLB Flip-Flops		93,000	184,000	93,000	184,000
Memory Resources	Maximum Distributed RAM (Kb)		692	1,355	692	1,355
	Block RAM (18 Kb each)		172	268	172	268
	Total Block RAM (Kb)		3,096	4,824	3,096	4,824
Clock Resources	Clock Management Tiles (CMT)		6	6	6	6
Embedded Hard IP Resources	DSP48A1 Slices		132	180	132	180
	Interface Blocks for PCI Express®		—	—	1	1
	Memory Controller Blocks		2	2	4	4
	GTP Low-Power Transceivers		—	—	8	8
Miscellaneous	Speed Grades: Extended (Q: -40°C - 125°C)		—	-2	-2	-2
	Speed Grades: Industrial (I: -40°C - 100°C)		-L1, -2	-L1, -2	-2, -3	-2, -3
	Package	Area	Maximum SelectIO™ Interface Pins (GTP Serial Transceivers)			
	CS(G)484 ^(1,2,3)	19 x 19 mm	328	338	292 (4)	296 (4)
	FG484 ^(2,3)	23 x 23 mm	270	338	268 (4)	296 (4)
	FG(G)676 ⁽¹⁾	27 x 27 mm			348 (8)	396 (8)

XMP076 (v2.3)

- Notes: 1. Pb-free (additional G) not available for Spartan-6 Q-temp devices.
 2. Devices in the CS(G)484 and FG484 support two memory controllers.
 3. Due to the GTP transceivers in the LXT devices, pinouts for the LX and LXT devices are not compatible.

Important: Verify all data in this document with the device data sheets found at www.xilinx.com