

S1D13U11

S1D13U11 SVGA USB Interface LCD Controller

The S1D13U11 is a SVGA USB interface LCD controller with an external SDRAM interface. The external SDRAM interface allows for support of a variety of memory sizes for the display buffer.

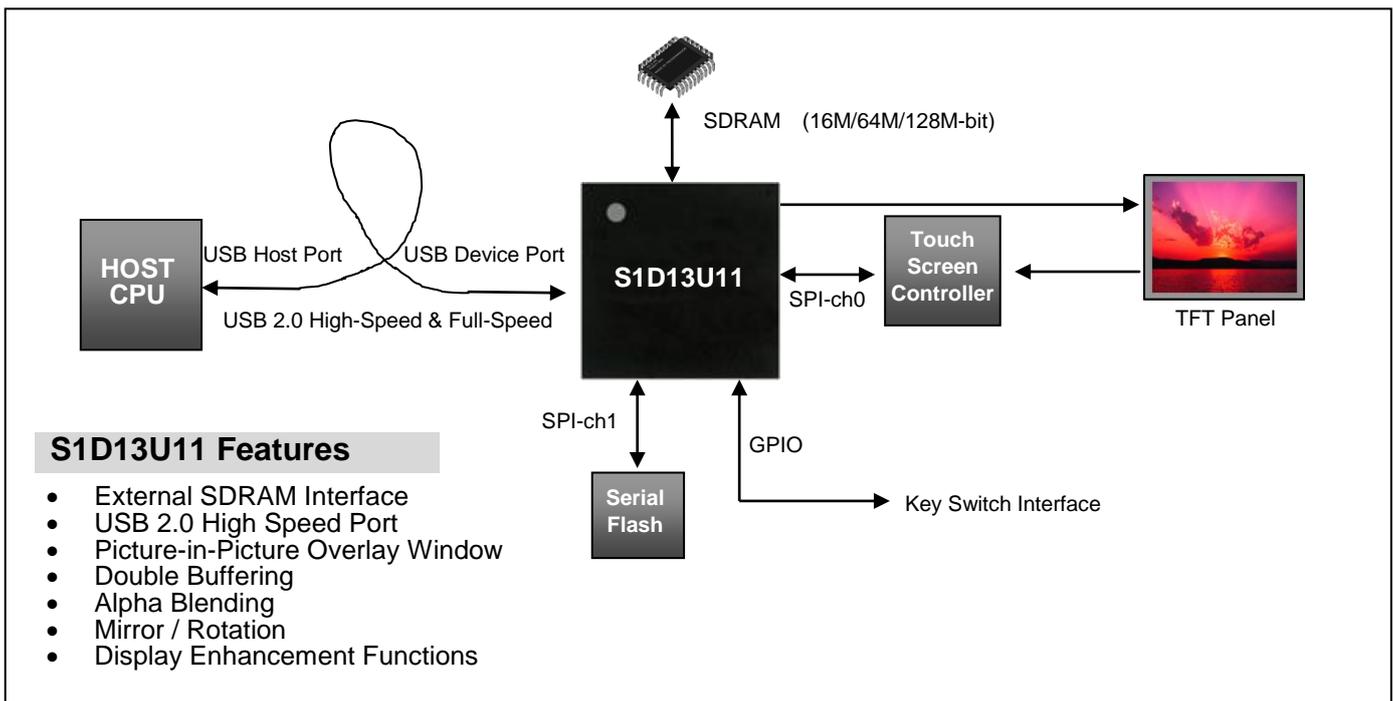
The S1D13U11 supports a USB2.0 High-speed device port interface and provides high performance bandwidth to external SDRAM, allowing for fast screen updates. Supporting TFT panels up to a SVGA resolutions (800x600), it can output either 24 bpp (RGB888) or 16 bpp (RGB565). The design also includes display features such as Picture-in-Picture, Double-Buffering, Alpha Blending, and Mirror/Rotation functions.

Additionally the S1D13U11 supports one I2C and two SPI serial interfaces allowing it to be connected to an external touch screen controller and serial flash ROM. The S1D13U11 is an ideal choice for a wide variety of application requiring an LCD controller to connect between a host CPU and LCD panel via the USB port.

FEATURES

- High performance SDRAM controller
- External 16/64/128M-bit SDRAM memory support
- USB 2.0 High-speed device port (480Mbps)
- Embedded USB protocol sequencer
- Input data formats:
 - RGB 8:8:8
 - RGB 5:6:5
- Supports TFT panels:
 - 16/18/24-bit interfaces
- Resolutions up to 800x600 @ 24 bpp
- I2C master and two SPI master interfaces
- Main and two Picture-in-Picture windows
- Multi-buffer display or Double-buffer display
- Alpha Blending
- Mirror / Rotation functions
- Display Enhancement features
- PWM output for LED backlight control
- Internal 12M/24M oscillator and PLL
- 8x8 Hardware Key scan interface
- Power:
 - USB and IO 3.3 volts
 - CORE and PLL 1.8 volts
- QFP20-144

SYSTEM BLOCK DIAGRAM



DESCRIPTION

Host Interface

- USB 2.0 High-speed device port (1-port)
- Endpoint
 - Five embedded endpoint FIFO
- Embedded Protocol sequencer
- RGB 8:8:8, RGB 5:6:5

Display Memory

- External SDRAM memory support:
 - 16/64/128M-bit
 - Maximum 96MHz SDRAM clock
 - 16-bit bus width

Display Support

- TFT panels:
 - 16/18/24-bit interface
- Supports resolutions up to 800x600 @ 24 bpp
- 24 bit-per-pixel (bpp) or 16 bpp color depths

Display Features

- Two Picture-in-Picture windows
- Software multi-buffer, Hardware double-buffer display
- Alpha Blending
- Mirror / Rotation functions
- Display Enhancement features
- Virtual display with smooth scroll

Miscellaneous

- I2C master interface
- Two SPI master interface
- Key scan interface (8x8, 8x4 or 8x2)
- PWM output for LED backlight control
- Buzzer output for touch screen feedback
- 12MHz or 24MHz crystal oscillator
- Internal programmable PLL (Maximum 96MHz)
- USBVDD 3.3 volts, IOVDD 3.3 volts and CORE/PLLVD 1.8 volts
- QFP20 144-pin package

JAPAN

SEIKO EPSON CORPORATION

IC Sales & Marketing Department
421-8 Hino, Hino-shi, Tokyo 191-8501, JAPAN
Phone: +81-42-587-5816 FAX: +81-42-587-5117

CHINA

EPSON (CHINA) CO., LTD

4th Floor, Tower 1 of China Central Place, 81 Jianguo Road,
Chaoyang District, Beijing 100025, CHINA
Phone: +86-10-8522-1199 FAX: +86-10-8522-1120

TAIWAN

EPSON TAIWAN TECHNOLOGY & TRADING LTD.

14F, No. 7, Song Ren Road,
Taipei 110, TAIWAN
Phone: +886-2-8786-6688 FAX: +886-2-8786-6660

AMERICA

EPSON ELECTRONICS AMERICA, INC.

214 Devcon Drive, San Jose, CA 95112,
USA
Phone: +1-800-228-3964 FAX: +1-408-922-0238

CHINA

SHANGHAI Branch

Room 01 & 04 17F, Greenland Center II, 562 Dong An Road,
Shanghai 200032, CHINA
Phone: +86-21-5423-5577 FAX: +86-21-5423-4677

SINGAPORE

EPSON SINGAPORE PTE., LTD.

1 HarbourFront Place,
#03-02 HarbourFront Tower One, SINGAPORE 098633
Phone: +65-6586-5500 FAX: +65-6271-3182

EUROPE

EPSON EUROPE ELECTRONICS GmbH

Riesstrasse 15, 80992 Munich,
GERMANY
Phone: +49-89-14005-0 FAX: +49-89-14005-110

CHINA

SHENZHEN Branch

Room 804-805, 8th Floor, Tower 2, Ali Center,
No.3331 Keyuan South RD (Shenzhen bay), Nanshan District,
Shenzhen 518054, CHINA
Phone: +86-10-3299-0588 FAX: +86-10-3299-0560

KOREA

SEIKO EPSON CORP.

19F, KLI 63 Bldg., 60 Yoido-dong,
Youngdeungpo-Ku, Seoul 150-763, KOREA
Phone: +82-2-784-6027 FAX: +82-2-767-3677

<http://global.epson.com/products/semicon>



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