

# S1D13705

## S1D13705 QVGA LCD Controller

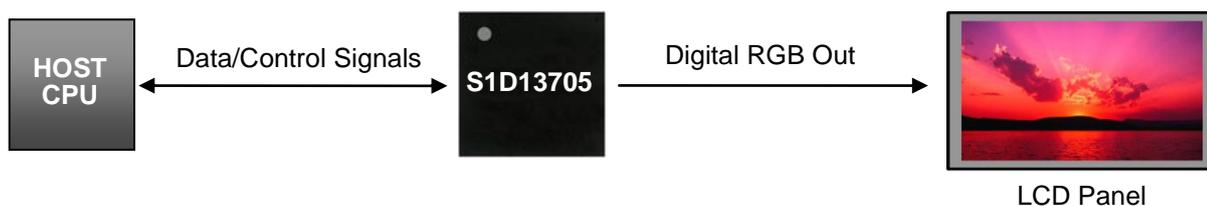
The S1D13705 is a LCD graphics controller with an embedded 80 KB SRAM display buffer. Targeting QVGA designs, the high integration of the S1D13705 provides a low cost, low power, single chip solution to meet the requirements of embedded markets such as office automation equipment, building equipment, and measuring devices where board size and battery life are major concerns.

Products requiring a rotated display can take advantage of the SwivelView 90° feature of the S1D13705. Virtual display, split screen, and double buffering are just some of the additional features supported. The S1D13705's impartiality to CPU type or operating system makes it an ideal display solution for a wide variety of applications.

### FEATURES

- Embedded 80 KB SRAM display buffer
- Direct support for most popular Host Interfaces:
  - Flexible host interface options
  - Direct Addressing
  - 16-bit data bus
- Resolutions up to:
  - 640x480 at a color depth of 2 bpp
  - 640x240 at a color depth of 4 bpp
  - 320x240 at a color depth of 8 bpp
- TFT LCD panels
- Passive LCD panels
- SwivelView™ 90° Hardware Rotation
- Split screen display
- Virtual display support
- Double buffering
- LCD power-down sequencing
- QFP14-80pin package

### SYSTEM BLOCK DIAGRAM



#### S1D13705 Features

- 80 KB SRAM
- SwivelView 90° Rotation
- Virtual Display
- Double Buffering



## DESCRIPTION

### Display Buffer

- Embedded 80 KB SRAM display buffer

### CPU Interface

- Direct support for most popular Host Interfaces:
  - Flexible host interface options
  - Direct addressing
  - 16-bit data bus
- CPU write buffer

### Power Down Modes

- Software suspend mode
- LCD power-down sequencing

### Clock Source

- Single clock input for both pixel and memory clocks
- Clock source can be internally divided down for a higher frequency clock input
- Dynamic switching of memory clocks in portrait mode

### Operating Voltage

- COREVDD 2.7 to 3.6; IOVDD 2.7 to 5.5 volts

### Package

- QFP14-80pin

### Display Support

- TFT interface panels
- Single-panel, single-drive passive displays
- Dual-panel, dual-drive passive displays
- 4/8-bit monochrome LCD interface
- 4/8-bit color LCD interface
- Example resolutions:
  - 640x480 at a color depth of 2 bpp
  - 640x240 at a color depth of 4 bpp
  - 320x240 at a color depth of 8 bpp

### Display Modes

- 1/2/4/8 bit-per-pixel (bpp) support on LCD
- Up to 16 shades of gray using FRM on monochrome passive LCD panels
- Up to 256 simultaneous colors from a possible 4096 colors on passive STN and active matrix TFT LCD panels
- Split screen display: allows two different images to be simultaneously viewed on the same display
- Virtual display support: displays images larger than the display size through the use of panning
- Double buffering/multi-pages: provides smooth animation and instantaneous screen update
- Hardware portrait mode: direct hardware 90° rotation of display image for portrait mode display

#### JAPAN SEIKO EPSON CORPORATION

IC Sales & Marketing Department  
421-8 Hino, Hino-shi, Tokyo 191-8501, JAPAN  
Phone: +81-42-587-5816 FAX: +81-42-587-5117

#### CHINA EPSON (CHINA) CO., LTD

4<sup>th</sup> Floor, Tower 1 of China Central Place, 81 Jianguo Road,  
Chaoyang District, Beijing 100025, CHINA  
Phone: +86-10-8522-1199 FAX: +86-10-8522-1120

#### TAIWAN EPSON TAIWAN TECHNOLOGY & TRADING LTD.

14F, No. 7, Song Ren Road,  
Taipei 110, TAIWAN  
Phone: +886-2-8786-6688 FAX: +886-2-8786-6660

#### AMERICA EPSON ELECTRONICS AMERICA, INC.

214 Devcon Drive, San Jose, CA 95112,  
USA  
Phone: +1-800-228-3964 FAX: +1-408-922-0238

#### CHINA SHANGHAI Branch

Room 01 & -04 17F, Greenland Center II, 562 Dong An Road,  
Shanghai 200032, CHINA  
Phone: +86-21-5423-5577 FAX: +86-21-5423-4677

#### SINGAPORE EPSON SINGAPORE PTE., LTD.

1 HarbourFront Place,  
#03-02 HarbourFront Tower One, SINGAPORE 098633  
Phone: +65-6586-5500 FAX: +65-6271-3182

#### EUROPE EPSON EUROPE ELECTRONICS GmbH

Riesstrasse 15, 80992 Munich,  
GERMANY  
Phone: +49-89-14005-0 FAX: +49-89-14005-110

#### CHINA SHENZHEN Branch

Room 804-805, 8<sup>th</sup> Floor, Tower 2, Ali Center,  
No.3331 Keyuan South RD (Shenzhen bay), Nanshan District,  
Shenzhen 518054, CHINA  
Phone: +86-10-3299-0588 FAX: +86-10-3299-0560

#### KOREA SEIKO EPSON CORP.

19F, KLI 63 Bldg., 60 Yoido-dong,  
Yongdeungpo-Ku, Seoul 150-763, KOREA  
Phone: +82-2-784-6027 FAX: +82-2-767-3677

<http://global.epson.com/products/semicon>



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